

Bellsouth Monthly State Summary Kentucky, December 2002

	Benchmark/ Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
Resale - Ordering									
% Rejected Service Requests - Mechanized									
A.1.1.1	Diagnostic			12.40%	5,104				Diagnostic
A.1.1.2	Diagnostic			33.91%	115				Diagnostic
A.1.1.3	Diagnostic								Diagnostic
A.1.1.4	Diagnostic								Diagnostic
A.1.1.5	Diagnostic								Diagnostic
A.1.1.6	Diagnostic								Diagnostic
% Rejected Service Requests - Partially Mechanized									
A.1.2.1	Diagnostic			13.18%	1,973				Diagnostic
A.1.2.2	Diagnostic			35.59%	163				Diagnostic
A.1.2.3	Diagnostic								Diagnostic
A.1.2.4	Diagnostic								Diagnostic
A.1.2.5	Diagnostic								Diagnostic
A.1.2.6	Diagnostic								Diagnostic
% Rejected Service Requests - Non-Mechanized									
A.1.3.1	Diagnostic			38.45%	65				Diagnostic
A.1.3.2	Diagnostic			56.82%	44				Diagnostic
A.1.3.3	Diagnostic			47.83%	23				Diagnostic
A.1.3.4	Diagnostic			33.33%	3				Diagnostic
A.1.3.5	Diagnostic			83.33%	6				Diagnostic
A.1.3.6	Diagnostic			50.00%	4				Diagnostic
Reject Interval - Mechanized									
A.1.4.1	>= 97% w in 1 hr			99.53%	633				YES
A.1.4.2	>= 97% w in 1 hr			100.00%	39				YES
A.1.4.3	>= 97% w in 1 hr								
A.1.4.4	>= 97% w in 1 hr								
A.1.4.5	>= 97% w in 1 hr								
A.1.4.6	>= 97% w in 1 hr								
Reject Interval - Partially Mechanized - 10 hours									
A.1.7.1	>= 85% w in 10 hrs			95.40%	261				YES
A.1.7.2	>= 85% w in 10 hrs			96.55%	58				YES
A.1.7.3	>= 85% w in 10 hrs								
A.1.7.4	>= 85% w in 10 hrs								
A.1.7.5	>= 85% w in 10 hrs								
A.1.7.6	>= 85% w in 10 hrs								
Reject Interval - Non-Mechanized									
A.1.8.1	>= 85% w in 24 hrs			92.59%	27				YES
A.1.8.2	>= 85% w in 24 hrs			100.00%	25				YES
A.1.8.3	>= 85% w in 24 hrs			100.00%	11				YES
A.1.8.4	>= 85% w in 24 hrs			100.00%	1				YES
A.1.8.5	>= 85% w in 24 hrs			100.00%	5				YES
A.1.8.6	>= 85% w in 24 hrs			100.00%	2				YES
FOC Timeliness - Mechanized									
A.1.9.1	>= 95% w in 3 hrs			99.89%	4,469				YES
A.1.9.2	>= 95% w in 3 hrs			100.00%	77				YES
A.1.9.3	>= 95% w in 3 hrs								
A.1.9.4	>= 95% w in 3 hrs								
A.1.9.5	>= 95% w in 3 hrs								
A.1.9.6	>= 95% w in 3 hrs								
FOC Timeliness - Partially Mechanized - 10 hours									
A.1.12.1	>= 85% w in 10 hrs			99.02%	1,742				YES

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		Benchmark Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
A1122	Business/KY (%)	>= 85% w in 10 hrs			96.58%	117				YES
A1123	Design (Specials)/KY (%)	>= 85% w in 10 hrs								
A1124	PBX/KY (%)	>= 85% w in 10 hrs								
A1125	Centrex/KY (%)	>= 85% w in 10 hrs								
A1126	ISDN/KY (%)	>= 85% w in 10 hrs								

FOC Timeliness - Non-Mechanized

A1131	Residence/ED/KY (%)	>= 85% w in 36 hrs			100.00%	40				YES
A1132	Business/KY (%)	>= 85% w in 36 hrs			100.00%	21				YES
A1133	Design (Specials)/KY (%)	>= 85% w in 36 hrs			100.00%	12				YES
A1134	PBX/KY (%)	>= 85% w in 36 hrs			100.00%	1				YES
A1135	Centrex/KY (%)	>= 85% w in 36 hrs			100.00%	1				YES
A1136	ISDN/KY (%)	>= 85% w in 36 hrs								

FOC & Reject Response Completeness - Mechanized

A11411	Residence/ED/KY (%)	>= 95%			100.00%	24				YES
A11412	Residence/TAG/KY (%)	>= 95%			99.86%	5,080				YES
A11421	Business/ED/KY (%)	>= 95%			100.00%	3				YES
A11422	Business/TAG/KY (%)	>= 95%			100.00%	112				YES
A11431	Design (Specials)/ED/KY (%)	>= 95%								
A11432	Design (Specials)/TAG/KY (%)	>= 95%								
A11441	PBX/ED/KY (%)	>= 95%								
A11442	PBX/TAG/KY (%)	>= 95%								
A11451	Centrex/ED/KY (%)	>= 95%								
A11452	Centrex/TAG/KY (%)	>= 95%								
A11461	ISDN/ED/KY (%)	>= 95%								
A11462	ISDN/TAG/KY (%)	>= 95%								

FOC & Reject Response Completeness - Partially Mechanized

A11511	Residence/ED/KY (%)	>= 95%			100.00%	13				YES
A11512	Residence/TAG/KY (%)	>= 95%			99.90%	1,960				YES
A11521	Business/ED/KY (%)	>= 95%			100.00%	163				YES
A11522	Business/TAG/KY (%)	>= 95%								
A11531	Design (Specials)/ED/KY (%)	>= 95%								
A11532	Design (Specials)/TAG/KY (%)	>= 95%								
A11541	PBX/ED/KY (%)	>= 95%								
A11542	PBX/TAG/KY (%)	>= 95%								
A11551	Centrex/ED/KY (%)	>= 95%								
A11552	Centrex/TAG/KY (%)	>= 95%								
A11561	ISDN/ED/KY (%)	>= 95%								
A11562	ISDN/TAG/KY (%)	>= 95%								

FOC & Reject Response Completeness - Non-Mechanized

A1161	Residence/ED/KY (%)	>= 95%			100.00%	65				YES
A1162	Business/KY (%)	>= 95%			100.00%	44				YES
A1163	Design (Specials)/KY (%)	>= 95%			100.00%	23				YES
A1164	PBX/KY (%)	>= 95%			100.00%	3				YES
A1165	Centrex/KY (%)	>= 95%			75.00%	6				NO
A1166	ISDN/KY (%)	>= 95%				4				

FOC & Reject Response Completeness (Multiple Responses) - Mechanized

A11711	Residence/ED/KY (%)	>= 95%			100.00%	24				YES
A11712	Residence/TAG/KY (%)	>= 95%			99.90%	5,073				YES
A11721	Business/ED/KY (%)	>= 95%			100.00%	3				YES
A11722	Business/TAG/KY (%)	>= 95%			99.11%	112				YES
A11731	Design (Specials)/ED/KY (%)	>= 95%								
A11732	Design (Specials)/TAG/KY (%)	>= 95%								
A11741	PBX/ED/KY (%)	>= 95%								
A11742	PBX/TAG/KY (%)	>= 95%								
A11751	Centrex/ED/KY (%)	>= 95%								
A11752	Centrex/TAG/KY (%)	>= 95%								

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A.2.12.5.1.1	P-9	Centrex<10 circuits/DispatchKY (%)
A.2.12.5.1.2	P-9	Centrex<10 circuits/Non-DispatchKY (%)
A.2.12.5.2.1	P-9	Centrex/>=10 circuits/DispatchKY (%)
A.2.12.5.2.2	P-9	Centrex/>=10 circuits/Non-DispatchKY (%)
A.2.12.6.1.1	P-9	ISDN<10 circuits/DispatchKY (%)
A.2.12.6.1.2	P-9	ISDN<10 circuits/Non-DispatchKY (%)
A.2.12.6.2.1	P-9	ISDN/>=10 circuits/DispatchKY (%)
A.2.12.6.2.2	P-9	ISDN/>=10 circuits/Non-DispatchKY (%)

Benchmark/
Analogs

BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
5.63%	71	0.00%	1	28.079	2.09887	2.4498	YES
0.79%	3,020	0.37	186	11.918	0.18589	2.8260	YES
20.00%	5	0.02	1	0.000	0.00000		YES
50.00%	2						
2.38%	84	100.00%	1				NO
0.21%	968	0.00%	9				YES
0.00%	38	0.00%	1				YES
0.00%	9						YES

A.2.14.1.1.1	P-5	Residence<10 circuits/DispatchKY (hours)
A.2.14.1.1.2	P-5	Residence<10 circuits/Non-DispatchKY (hours)
A.2.14.1.2.1	P-5	Residence/>=10 circuits/DispatchKY (hours)
A.2.14.1.2.2	P-5	Residence/>=10 circuits/Non-DispatchKY (hours)
A.2.14.2.1.1	P-5	Business<10 circuits/DispatchKY (hours)
A.2.14.2.1.2	P-5	Business<10 circuits/Non-DispatchKY (hours)
A.2.14.2.2.1	P-5	Business/>=10 circuits/DispatchKY (hours)
A.2.14.3.1.1	P-5	Design (Specials)/<10 circuits/DispatchKY (hours)
A.2.14.3.1.2	P-5	Design (Specials)/<10 circuits/Non-DispatchKY (hours)
A.2.14.3.2.1	P-5	Design (Specials)/>=10 circuits/DispatchKY (hours)
A.2.14.3.2.2	P-5	Design (Specials)/>=10 circuits/Non-DispatchKY (hours)
A.2.14.4.1.1	P-5	PBX/<10 circuits/DispatchKY (hours)
A.2.14.4.1.2	P-5	PBX/<10 circuits/Non-DispatchKY (hours)
A.2.14.4.2.1	P-5	PBX/>=10 circuits/DispatchKY (hours)
A.2.14.4.2.2	P-5	PBX/>=10 circuits/Non-DispatchKY (hours)
A.2.14.5.1.1	P-5	Centrex<10 circuits/DispatchKY (hours)
A.2.14.5.1.2	P-5	Centrex<10 circuits/Non-DispatchKY (hours)
A.2.14.5.2.1	P-5	Centrex/>=10 circuits/DispatchKY (hours)
A.2.14.5.2.2	P-5	Centrex/>=10 circuits/Non-DispatchKY (hours)
A.2.14.6.1.1	P-5	ISDN<10 circuits/DispatchKY (hours)
A.2.14.6.1.2	P-5	ISDN<10 circuits/Non-DispatchKY (hours)
A.2.14.6.2.1	P-5	ISDN/>=10 circuits/DispatchKY (hours)
A.2.14.6.2.2	P-5	ISDN/>=10 circuits/Non-DispatchKY (hours)

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BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
5.16	4,737	0.02	186	28.079	2.09887	2.4498	YES
0.89	116,901	0.37	4,260	11.918	0.18589	2.8260	YES
0.02	13	0.02	1	0.000	0.00000		YES
2.64	1,409	0.02	32	15.187	2.71498	0.9640	YES
1.30	6,611	0.59	108	15.169	1.47147	0.4825	YES
1.08	15			3.864			
5.38	397			17.170			
0.94	73			3.686			
17.82	40			38.541			
11.51	6			8.943			
18.34	25			90.543			
24.95	11			20.675			
60.11	7			152.414			
0.59	93			2.763			
8.19	79			47.926			
0.02	6			0.000			
0.42	2			0.047			
15.96	70	0.38	2	30.342			
3.99	61			27.485	19.75102	0.1831	YES
24.07	16			55.951			
0.13	3			0.202			

A.2.15.1.1.1	P-5	Residence<10 circuits/DispatchKY (hours)
A.2.15.1.1.2	P-5	Residence<10 circuits/Non-DispatchKY (hours)
A.2.15.1.2.1	P-5	Residence/>=10 circuits/DispatchKY (hours)
A.2.15.1.2.2	P-5	Residence/>=10 circuits/Non-DispatchKY (hours)
A.2.15.2.1.1	P-5	Business<10 circuits/DispatchKY (hours)
A.2.15.2.1.2	P-5	Business<10 circuits/Non-DispatchKY (hours)
A.2.15.2.2.1	P-5	Business/>=10 circuits/DispatchKY (hours)
A.2.15.2.2.2	P-5	Business/>=10 circuits/Non-DispatchKY (hours)
A.2.15.3.1.1	P-5	Design (Specials)/<10 circuits/DispatchKY (hours)
A.2.15.3.1.2	P-5	Design (Specials)/<10 circuits/Non-DispatchKY (hours)
A.2.15.3.2.1	P-5	Design (Specials)/>=10 circuits/DispatchKY (hours)
A.2.15.3.2.2	P-5	Design (Specials)/>=10 circuits/Non-DispatchKY (hours)
A.2.15.4.1.1	P-5	PBX/<10 circuits/DispatchKY (hours)
A.2.15.4.1.2	P-5	PBX/<10 circuits/Non-DispatchKY (hours)
A.2.15.4.2.1	P-5	PBX/>=10 circuits/DispatchKY (hours)
A.2.15.4.2.2	P-5	PBX/>=10 circuits/Non-DispatchKY (hours)
A.2.15.5.1.1	P-5	Centrex<10 circuits/DispatchKY (hours)
A.2.15.5.1.2	P-5	Centrex<10 circuits/Non-DispatchKY (hours)
A.2.15.5.2.1	P-5	Centrex/>=10 circuits/DispatchKY (hours)
A.2.15.5.2.2	P-5	Centrex/>=10 circuits/Non-DispatchKY (hours)
A.2.15.6.1.1	P-5	ISDN<10 circuits/DispatchKY (hours)
A.2.15.6.1.2	P-5	ISDN<10 circuits/Non-DispatchKY (hours)
A.2.15.6.2.1	P-5	ISDN/>=10 circuits/DispatchKY (hours)
A.2.15.6.2.2	P-5	ISDN/>=10 circuits/Non-DispatchKY (hours)

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BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
16.27	21						
4.76	106						
39.32	1						
16.65	27						
15.18	1						
31.43	9						
31.19	2						
130.08	1						
14.00	1						
175.12	1						
46.15	1						

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A.2.25.3.1.1	P-11	Design (Specials)/<10 circuits/Non-Dispatch/KY (%)	>= 95%
A.2.25.3.1.2	P-11	Design (Specials)/>=10 circuits/Non-Dispatch/KY (%)	>= 95%
A.2.25.3.2.1	P-11	Design (Specials)/>=10 circuits/Non-Dispatch/KY (%)	>= 95%
A.2.25.3.2.2	P-11	Design (Specials)/>=10 circuits/Non-Dispatch/KY (%)	>= 95%

Resale - Maintenance and Repair

Missed Repair Appointments			
A.3.1.1.1	M&R-1	Residence/Dispatch/KY (%)	Res
A.3.1.1.2	M&R-1	Residence/Non-Dispatch/KY (%)	Res
A.3.1.2.1	M&R-1	Business/Dispatch/KY (%)	Bus
A.3.1.2.2	M&R-1	Business/Non-Dispatch/KY (%)	Bus
A.3.1.3.1	M&R-1	Design (Specials)/Dispatch/KY (%)	Design
A.3.1.3.2	M&R-1	Design (Specials)/Non-Dispatch/KY (%)	Design
A.3.1.4.1	M&R-1	PBX/Dispatch/KY (%)	PBX
A.3.1.4.2	M&R-1	PBX/Non-Dispatch/KY (%)	PBX
A.3.1.5.1	M&R-1	Centrex/Dispatch/KY (%)	Centrex
A.3.1.5.2	M&R-1	Centrex/Non-Dispatch/KY (%)	Centrex
A.3.1.6.1	M&R-1	ISDN/Dispatch/KY (%)	ISDN
A.3.1.6.2	M&R-1	ISDN/Non-Dispatch/KY (%)	ISDN

Customer Trouble Report Rate

A.3.2.1.1	M&R-2	Residence/Dispatch/KY (%)	Res
A.3.2.1.2	M&R-2	Residence/Non-Dispatch/KY (%)	Res
A.3.2.2.1	M&R-2	Business/Dispatch/KY (%)	Bus
A.3.2.2.2	M&R-2	Business/Non-Dispatch/KY (%)	Bus
A.3.2.3.1	M&R-2	Design (Specials)/Dispatch/KY (%)	Design
A.3.2.3.2	M&R-2	Design (Specials)/Non-Dispatch/KY (%)	Design
A.3.2.4.1	M&R-2	PBX/Dispatch/KY (%)	PBX
A.3.2.4.2	M&R-2	PBX/Non-Dispatch/KY (%)	PBX
A.3.2.5.1	M&R-2	Centrex/Dispatch/KY (%)	Centrex
A.3.2.5.2	M&R-2	Centrex/Non-Dispatch/KY (%)	Centrex
A.3.2.6.1	M&R-2	ISDN/Dispatch/KY (%)	ISDN
A.3.2.6.2	M&R-2	ISDN/Non-Dispatch/KY (%)	ISDN

Maintenance Average Duration

A.3.3.1.1	M&R-3	Residence/Dispatch/KY (hours)	Res
A.3.3.1.2	M&R-3	Residence/Non-Dispatch/KY (hours)	Res
A.3.3.2.1	M&R-3	Business/Dispatch/KY (hours)	Bus
A.3.3.2.2	M&R-3	Business/Non-Dispatch/KY (hours)	Bus
A.3.3.3.1	M&R-3	Design (Specials)/Dispatch/KY (hours)	Design
A.3.3.3.2	M&R-3	Design (Specials)/Non-Dispatch/KY (hours)	Design
A.3.3.4.1	M&R-3	PBX/Dispatch/KY (hours)	PBX
A.3.3.4.2	M&R-3	PBX/Non-Dispatch/KY (hours)	PBX
A.3.3.5.1	M&R-3	Centrex/Dispatch/KY (hours)	Centrex
A.3.3.5.2	M&R-3	Centrex/Non-Dispatch/KY (hours)	Centrex
A.3.3.6.1	M&R-3	ISDN/Dispatch/KY (hours)	ISDN
A.3.3.6.2	M&R-3	ISDN/Non-Dispatch/KY (hours)	ISDN

% Repeat Troubles within 30 Days

A.3.4.1.1	M&R-4	Residence/Dispatch/KY (%)	Res
A.3.4.1.2	M&R-4	Residence/Non-Dispatch/KY (%)	Res
A.3.4.2.1	M&R-4	Business/Dispatch/KY (%)	Bus
A.3.4.2.2	M&R-4	Business/Non-Dispatch/KY (%)	Bus
A.3.4.3.1	M&R-4	Design (Specials)/Dispatch/KY (%)	Design
A.3.4.3.2	M&R-4	Design (Specials)/Non-Dispatch/KY (%)	Design
A.3.4.4.1	M&R-4	PBX/Dispatch/KY (%)	PBX
A.3.4.4.2	M&R-4	PBX/Non-Dispatch/KY (%)	PBX
A.3.4.5.1	M&R-4	Centrex/Dispatch/KY (%)	Centrex
A.3.4.5.2	M&R-4	Centrex/Non-Dispatch/KY (%)	Centrex

Benchmark
Analog

BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
		94.34%	53		0.01621	0.9576	NO
		91.89%	74		0.01976	1.2028	NO
		75.00%	4		0.05885	2.3495	YES
		78.57%	14		0.05387	-1.8830	NO
					0.01672	0.1786	YES
					0.00000		YES
					0.06181	-2.9467	NO
					0.32314	0.3641	YES
					0.23407	0.2455	YES
					0.00000		YES
					0.09346	0.2675	YES

12.65%	16,117	11.11%	432		0.01621	0.9576	YES
2.36%	6,312	0.00%	60		0.01976	1.2028	YES
13.83%	2,025	0.00%	35		0.05885	2.3495	YES
2.36%	849	12.50%	8		0.05387	-1.8830	NO
0.30%	335	0.00%	11		0.01672	0.1786	YES
0.00%	356	0.00%	8		0.00000		YES
1.79%	36	20.00%	5		0.06181	-2.9467	NO
7.14%	42	0.00%	0				YES
11.76%	170	0.00%	1		0.32314	0.3641	YES
5.75%	87	0.00%	1		0.23407	0.2455	YES
0.00%	25	0.00%	2		0.00000		YES
2.50%	40	0.00%	3		0.09346	0.2675	YES

0.79%	796,787	2.50%	17,261		0.00109	-4.3868	NO
1.00%	202,049	0.99%	3,553		0.00068	6.4925	YES
0.42%	202,049	0.23%	3,553		0.00169	0.1012	YES
0.41%	82,484	1.40%	788		0.00110	1.7779	YES
0.46%	82,484	1.02%	788		0.00228	-4.3392	NO
0.16%	35,201	0.00%	740		0.00248	-2.5177	NO
0.12%	35,201	0.00%	740		0.00148	-3.4868	NO
0.30%	56,105	0.39%	255		0.00128	0.9299	YES
0.16%	56,105	0.39%	255		0.00345	-0.2580	YES
0.04%	62,395	0.44%	458		0.00247	-0.9563	YES
0.06%	62,395	0.66%	458		0.00094	-4.2249	YES
					0.00119	-4.9764	NO

28.48	16,117	23.23	432	36,444	1.77673	2.9547	YES
10.09	6,312	6.63	60	19,375	2.51317	1.3743	YES
13.35	2,025	9.54	35	19,173	3.26670	1.1651	YES
3.14	849	26.63	8	6,572	2.33454	-10.9164	NO
4.79	335	4.60	11	3,926	1.20315	0.1599	YES
2.21	396	2.19	8	3,171	1.13248	0.0195	YES
7.69	56	6.28	5	6,202	2.89462	0.4876	YES
6.37	42	0.00	0	7,830			YES
15.90	170	68.00	1	54,971	55.13273	-0.9450	YES
4.92	87	3.00	1	12,739	12.81220	0.1488	YES
5.78	25	12.22	2	3,605	2.64887	-2.4302	NO
4.65	40	3.28	3	8,708	5.21290	0.2615	YES

18.11%	16,117	15.97%	432		0.01877	1.1362	YES
13.17%	6,312	10.00%	60		0.04386	0.7217	YES
13.43%	2,025	5.71%	35		0.05813	1.3276	YES
10.84%	849	0.00%	8		0.11041	0.9814	YES
23.58%	335	18.18%	11		0.13008	0.4152	YES
18.69%	396	12.50%	8		0.13920	0.4445	YES
1.79%	56	0.00%	5		0.06181	0.2889	YES
9.52%	42	0.00%	0				YES
18.82%	170	0.00%	1		0.39205	0.4601	YES
16.09%	87	0.00%	1		0.39596	0.4354	YES

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A.3.4.6.1	MAR-4	ISDN/Dispatch/KY (%)	
A.3.4.6.2	MAR-4	ISDN/Non-Dispatch/KY (%)	

Benchmark/ Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
ISDN	16.00%	25	0.00%	2		0.26840	0.5939	YES
ISDN	15.00%	40	0.00%	3		0.21375	0.7018	YES

Out of Service > 24 hours

A.3.5.1.1	MAR-5	Residence/Dispatch/KY (%)	
A.3.5.1.2	MAR-5	Residence/Non-Dispatch/KY (%)	
A.3.5.2.1	MAR-5	Business/Dispatch/KY (%)	
A.3.5.2.2	MAR-5	Business/Non-Dispatch/KY (%)	
A.3.5.3.1	MAR-5	Design (Specials)/Dispatch/KY (%)	
A.3.5.3.2	MAR-5	Design (Specials)/Non-Dispatch/KY (%)	
A.3.5.4.1	MAR-5	PBX/Dispatch/KY (%)	
A.3.5.4.2	MAR-5	PBX/Non-Dispatch/KY (%)	
A.3.5.5.1	MAR-5	Centrex/Dispatch/KY (%)	
A.3.5.5.2	MAR-5	Centrex/Non-Dispatch/KY (%)	
A.3.5.6.1	MAR-5	ISDN/Dispatch/KY (%)	
A.3.5.6.2	MAR-5	ISDN/Non-Dispatch/KY (%)	

Res	15.07%	10,322	10.22%	313		0.02053	2.3630	YES
Res	7.10%	1,902	0.00%	31		0.04649	1.5286	YES
Bus	7.00%	1,242	0.00%	18		0.06059	1.1561	YES
Bus	2.45%	286	14.29%	7		0.05911	-2.0026	NO
Design	0.30%	335	0.00%	11		0.01672	0.1786	YES
Design	0.00%	396	0.00%	8		0.00000		YES
PBX	0.00%	53	0.00%	3		0.00000		YES
PBX	5.56%	36	0.00%	0		0.26656	-3.4656	NO
Centrex	7.62%	105	100.00%	1		0.00000		YES
Centrex	8.33%	24	0.00%	0		0.00000		YES
ISDN	0.00%	25	0.00%	2		0.00000		YES
ISDN	2.50%	40	0.00%	3		0.09346	0.2675	YES

Resale - Billing

Invoice Accuracy

A.4.1	B-1	KY (%)	
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BST - State

99.22%	\$97,330,611	97.91%	\$1,129,972	0.00008	157,3598	NO
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Mean Time to Deliver Invoices - CRIS

A.4.2	B-2	Region (business days)	
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BST - Region

4.14	1	3.26	1,989		YES
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B-1.9.3	Loop + Port Combinations/KY (%)	>= 95% w/in 3 hrs
B-1.9.4	Combo Other/KY (%)	>= 95% w/in 3 hrs
B-1.9.5	XDSL (ADSL, HDSL and UCLJ)/KY (%)	>= 95% w/in 3 hrs
B-1.9.6	ISDN Loop (UDN, UDC)/KY (%)	>= 95% w/in 3 hrs
B-1.9.7	Line Sharing/KY (%)	>= 95% w/in 3 hrs
B-1.9.8	2W Analog Loop Design/KY (%)	>= 95% w/in 3 hrs
B-1.9.9	2W Analog Loop Non-Design/KY (%)	>= 95% w/in 3 hrs
B-1.9.10	2W Analog Loop w/INP Design/KY (%)	>= 95% w/in 3 hrs
B-1.9.11	2W Analog Loop w/INP Non-Design/KY (%)	>= 95% w/in 3 hrs
B-1.9.12	2W Analog Loop w/INP Design/KY (%)	>= 95% w/in 3 hrs
B-1.9.13	2W Analog Loop w/INP Non-Design/KY (%)	>= 95% w/in 3 hrs
B-1.9.14	Other Design/KY (%)	>= 95% w/in 3 hrs
B-1.9.15	Other Non-Design/KY (%)	>= 95% w/in 3 hrs
B-1.9.16	INP Standalone/KY (%)	>= 95% w/in 3 hrs
B-1.9.17	INP Standalone/KY (%)	>= 95% w/in 3 hrs

FOC Timeliness - Partially Mechanized - 10 hours

O-9	Switch Ports/KY (%)	>= 85% w/in 10 hrs
O-9	Local Interface Transports/KY (%)	>= 85% w/in 10 hrs
O-9	Loop + Port Combinations/KY (%)	>= 85% w/in 10 hrs
O-9	Combo Other/KY (%)	>= 85% w/in 10 hrs
O-9	XDSL (ADSL, HDSL and UCLJ)/KY (%)	>= 85% w/in 10 hrs
O-9	ISDN Loop (UDN, UDC)/KY (%)	>= 85% w/in 10 hrs
O-9	Line Sharing/KY (%)	>= 85% w/in 10 hrs
O-9	2W Analog Loop Design/KY (%)	>= 85% w/in 10 hrs
O-9	2W Analog Loop Non-Design/KY (%)	>= 85% w/in 10 hrs
O-9	2W Analog Loop w/INP Design/KY (%)	>= 85% w/in 10 hrs
O-9	2W Analog Loop w/INP Non-Design/KY (%)	>= 85% w/in 10 hrs
O-15	2W Analog Loop w/INP Design/KY (%)	>= 85% w/in 10 hrs
O-15	2W Analog Loop w/INP Non-Design/KY (%)	>= 85% w/in 10 hrs
O-9	Other Design/KY (%)	>= 85% w/in 10 hrs
O-9	Other Non-Design/KY (%)	>= 85% w/in 10 hrs
O-9	INP Standalone/KY (%)	>= 85% w/in 10 hrs
O-15	INP Standalone/KY (%)	>= 85% w/in 10 hrs

FOC Timeliness - Non-Mechanized

O-9	Switch Ports/KY (%)	>= 85% w/in 36 hrs
O-9	Local Interface Transports/KY (%)	>= 85% w/in 36 hrs
O-9	Loop + Port Combinations/KY (%)	>= 85% w/in 36 hrs
O-9	Combo Other/KY (%)	>= 85% w/in 36 hrs
O-9	XDSL (ADSL, HDSL and UCLJ)/KY (%)	>= 85% w/in 36 hrs
O-9	ISDN Loop (UDN, UDC)/KY (%)	>= 85% w/in 36 hrs
O-9	Line Sharing/KY (%)	>= 85% w/in 36 hrs
O-9	2W Analog Loop Design/KY (%)	>= 85% w/in 36 hrs
O-9	2W Analog Loop Non-Design/KY (%)	>= 85% w/in 36 hrs
O-9	2W Analog Loop w/INP Design/KY (%)	>= 85% w/in 36 hrs
O-9	2W Analog Loop w/INP Non-Design/KY (%)	>= 85% w/in 36 hrs
O-15	2W Analog Loop w/INP Design/KY (%)	>= 85% w/in 36 hrs
O-15	2W Analog Loop w/INP Non-Design/KY (%)	>= 85% w/in 36 hrs
O-9	Other Design/KY (%)	>= 85% w/in 36 hrs
O-9	Other Non-Design/KY (%)	>= 85% w/in 36 hrs
O-9	INP Standalone/KY (%)	>= 85% w/in 36 hrs
O-15	INP Standalone/KY (%)	>= 85% w/in 36 hrs

FOC & Reject Response Completeness - Mechanized

B-1.14.1.1	Switch Ports/EDJ/KY (%)	>= 95%
B-1.14.1.2	Switch Ports/TAG/KY (%)	>= 95%
B-1.14.2.1	Local Interface Transports/EDJ/KY (%)	>= 95%
B-1.14.2.2	Local Interface Transports/TAG/KY (%)	>= 95%
B-1.14.3.1	Loop + Port Combinations/EDJ/KY (%)	>= 95%

Benchmark/ Analog

BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
		99.86%	10,095				YES
		100.00%	71				YES
		100.00%	2				YES
		100.00%	66				YES
		100.00%	30				YES
		100.00%	45				YES
		99.93%	2,836				YES
		99.94%	1,700				YES

		95.44%	1,557				YES
		100.00%	7				YES
		50.00%	2				NO
		100.00%	6				YES
		92.86%	14				YES
		100.00%	9				YES
		92.59%	27				YES
		90.91%	77				YES
		99.77%	434				YES

		100.00%	1				YES
		97.46%	118				YES
		100.00%	4				YES
		100.00%	6				YES
		100.00%	1				YES
		100.00%	3				YES
		100.00%	7				YES
		100.00%	5				YES
		100.00%	1				YES
		95.45%	22				YES
		98.28%	58				YES
		100.00%	8				YES
		100.00%	95				YES

		99.97%	7,540				YES

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Item	FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized	Benchmark/Analogy	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.18.1.1	Switch Ports/EDIKY (%)	>= 95%								
B.1.18.1.2	Switch Ports/TAGKY (%)	>= 95%								
B.1.18.2.1	Local Interoffice Transport/EDIKY (%)	>= 95%								
B.1.18.2.2	Local Interoffice Transport/TAGKY (%)	>= 95%								
B.1.18.3.1	Loop + Port Combinations/EDIKY (%)	>= 95%			95.71%	885				YES
B.1.18.3.2	Loop + Port Combinations/TAGKY (%)	>= 95%			87.47%	1,109				NO
B.1.18.4.1	Combo Other/EDIKY (%)	>= 95%								
B.1.18.4.2	Combo Other/TAGKY (%)	>= 95%								
B.1.18.5.1	XDSL (ADSL, HDSL and UCL)/EDIKY (%)	>= 95%			100.00%	8				YES
B.1.18.5.2	XDSL (ADSL, HDSL and UCL)/TAGKY (%)	>= 95%			100.00%	4				YES
B.1.18.6.1	ISDN Loop (UDN, UDC)/EDIKY (%)	>= 95%			100.00%	2				YES
B.1.18.6.2	ISDN Loop (UDN, UDC)/TAGKY (%)	>= 95%			100.00%	2				YES
B.1.18.7.1	Line Sharing/EDIKY (%)	>= 95%			87.50%	8				NO
B.1.18.7.2	Line Sharing/TAGKY (%)	>= 95%			50.00%	2				NO
B.1.18.8.1	2W Analog Loop Design/EDIKY (%)	>= 95%			100.00%	14				YES
B.1.18.8.2	2W Analog Loop Design/TAGKY (%)	>= 95%			100.00%	10				YES
B.1.18.9.1	2W Analog Loop Non-Design/EDIKY (%)	>= 95%								
B.1.18.9.2	2W Analog Loop Non-Design/TAGKY (%)	>= 95%								
B.1.18.10.1	2W Analog Loop w/INP Design/EDIKY (%)	>= 95%								
B.1.18.10.2	2W Analog Loop w/INP Design/TAGKY (%)	>= 95%								
B.1.18.11.1	2W Analog Loop w/INP Non-Design/EDIKY (%)	>= 95%								
B.1.18.11.2	2W Analog Loop w/INP Non-Design/TAGKY (%)	>= 95%								
B.1.18.12.1	2W Analog Loop w/INP Design/EDIKY (%)	>= 95%								
B.1.18.12.2	2W Analog Loop w/INP Design/TAGKY (%)	>= 95%								
B.1.18.13.1	2W Analog Loop w/INP Non-Design/EDIKY (%)	>= 95%								
B.1.18.13.2	2W Analog Loop w/INP Non-Design/TAGKY (%)	>= 95%								
B.1.18.14.1	Other Design/EDIKY (%)	>= 95%			94.12%	17				NO
B.1.18.14.2	Other Design/TAGKY (%)	>= 95%			100.00%	14				YES
B.1.18.15.1	Other Non-Design/EDIKY (%)	>= 95%			100.00%	108				YES
B.1.18.15.2	Other Non-Design/TAGKY (%)	>= 95%			89.47%	19				NO
B.1.18.16.1	INP Standalone/EDIKY (%)	>= 95%								
B.1.18.16.2	INP Standalone/TAGKY (%)	>= 95%								
B.1.18.17.1	LNP Standalone/EDIKY (%)	>= 95%								
B.1.18.17.2	LNP Standalone/TAGKY (%)	>= 95%			97.24%	507				YES

Item	FOC & Reject Response Completeness (Multiple Responses) - Non-Mechanized	Benchmark/Analogy	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.19.1	Switch Ports/KY (%)	>= 95%								
B.1.19.2	Local Interoffice Transport/KY (%)	>= 95%								YES
B.1.19.3	Local Interoffice Transport/TAGKY (%)	>= 95%			94.98%	239				NO
B.1.19.4	Combo Other/KY (%)	>= 95%			100.00%	11				YES
B.1.19.5	XDSL (ADSL, HDSL and UCL)/KY (%)	>= 95%			100.00%	6				YES
B.1.19.6	ISDN Loop (UDN, UDC)/KY (%)	>= 95%			100.00%	2				YES
B.1.19.7	Line Sharing/KY (%)	>= 95%			100.00%	9				YES
B.1.19.8	2W Analog Loop Design/KY (%)	>= 95%			100.00%	14				YES
B.1.19.9	2W Analog Loop Design/TAGKY (%)	>= 95%			100.00%	14				YES
B.1.19.10	2W Analog Loop Non-Design/KY (%)	>= 95%								
B.1.19.11	2W Analog Loop Non-Design/TAGKY (%)	>= 95%								
B.1.19.12	2W Analog Loop w/INP Design/KY (%)	>= 95%								
B.1.19.13	2W Analog Loop w/INP Design/TAGKY (%)	>= 95%								
B.1.19.14	2W Analog Loop w/INP Non-Design/KY (%)	>= 95%			100.00%	1				YES
B.1.19.15	Other Design/KY (%)	>= 95%			84.62%	26				NO
B.1.19.16	Other Non-Design/KY (%)	>= 95%			93.51%	77				NO
B.1.19.17	INP Standalone/KY (%)	>= 95%			100.00%	10				YES
B.1.19.17	LNP Standalone/KY (%)	>= 95%			99.14%	116				YES

Order Completion Interval	R&B (POTS)	7.54	5.440	10.794
P.4 Switch Ports<10 Circuits/Dispatch/KY (days)				

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B.2.1.1.1.2	P-4	Switch Ports<10 circuits/Non-Dispatch/KY (days)	R&B (POTS)
B.2.1.1.2.1	P-4	Switch Ports>=10 circuits/Dispatch/KY (days)	R&B (POTS)
B.2.1.1.2.2	P-4	Local Interface Transport<10 circuits/Dispatch/KY (days)	R&B (POTS)
B.2.1.2.1.1	P-4	Local Interface Transport<10 circuits/Non-Dispatch/KY (days)	DS1/DS3
B.2.1.2.1.2	P-4	Local Interface Transport>=10 circuits/Dispatch/KY (days)	DS1/DS3
B.2.1.2.2.1	P-4	Local Interface Transport>=10 circuits/Non-Dispatch/KY (days)	DS1/DS3
B.2.1.3.1.1	P-4	Loop + Port Combinations<10 circuits/Dispatch/KY (days)	R&B
B.2.1.3.1.2	P-4	Loop + Port Combinations<10 circuits/Non-Dispatch/KY (days)	R&B
B.2.1.3.1.3	P-4	Loop + Port Combinations<10 circuits/Switch Based Orders/KY (days)	R&B
B.2.1.3.1.4	P-4	Loop + Port Combinations<10 circuits/Dispatch InlKY (days)	R&B
B.2.1.3.2.1	P-4	Loop + Port Combinations>=10 circuits/Dispatch/KY (days)	R&B
B.2.1.3.2.2	P-4	Loop + Port Combinations>=10 circuits/Non-Dispatch/KY (days)	R&B
B.2.1.3.2.3	P-4	Loop + Port Combinations>=10 circuits/Switch Based Orders/KY (days)	R&B
B.2.1.3.2.4	P-4	Loop + Port Combinations>=10 circuits/Dispatch InlKY (days)	R&B
B.2.1.4.1.1	P-4	Combo Other/<10 circuits/Dispatch/KY (days)	R&B&D - Disp
B.2.1.4.1.4	P-4	Combo Other/>=10 circuits/Dispatch/KY (days)	R&B&D - Disp
B.2.1.4.2.1	P-4	Combo Other/<10 circuits/Dispatch InlKY (days)	R&B&D - Disp
B.2.1.4.2.4	P-4	Combo Other/>=10 circuits/Dispatch InlKY (days)	R&B&D - Disp
B.2.1.6.3.1	P-4	UNE ISDN/<6 circuits/Non-Dispatch/KY (days)	ISDN - BRI
B.2.1.6.3.2	P-4	UNE ISDN/<6 circuits/Dispatch/KY (days)	ISDN - BRI
B.2.1.6.4.1	P-4	UNE ISDN/<6-13 circuits/Dispatch/KY (days)	ISDN - BRI
B.2.1.6.4.2	P-4	UNE ISDN/<6-13 circuits/Non-Dispatch/KY (days)	ISDN - BRI
B.2.1.6.5.1	P-4	UNE ISDN/>=14 circuits/Dispatch/KY (days)	ISDN - BRI
B.2.1.6.5.2	P-4	UNE ISDN/>=14 circuits/Non-Dispatch/KY (days)	ISDN - BRI
B.2.1.7.3.1	P-4	Line Sharing/<6 circuits/Dispatch/KY (days)	ADSL to Retail
B.2.1.7.3.2	P-4	Line Sharing/<6 circuits/Non-Dispatch/KY (days)	ADSL to Retail
B.2.1.7.4.1	P-4	Line Sharing/>=13 circuits/Dispatch/KY (days)	ADSL to Retail
B.2.1.7.4.2	P-4	Line Sharing/>=13 circuits/Non-Dispatch/KY (days)	ADSL to Retail
B.2.1.7.5.1	P-4	Line Sharing/>=14 circuits/Dispatch/KY (days)	ADSL to Retail
B.2.1.7.5.2	P-4	Line Sharing/>=14 circuits/Non-Dispatch/KY (days)	ADSL to Retail
B.2.1.8.1.1	P-4	2W Analog Loop Design<10 circuits/Dispatch/KY (days)	R&B - Disp
B.2.1.8.1.2	P-4	2W Analog Loop Design<10 circuits/Non-Dispatch/KY (days)	R&B - Disp
B.2.1.8.2.1	P-4	2W Analog Loop Design>=10 circuits/Dispatch/KY (days)	R&B - Disp
B.2.1.8.2.2	P-4	2W Analog Loop Design>=10 circuits/Non-Dispatch/KY (days)	R&B - Disp
B.2.1.9.1.1	P-4	2W Analog Loop Non-Design<10 circuits/Dispatch/KY (days)	R&B (POTS) excd SB Or
B.2.1.9.1.4	P-4	2W Analog Loop Non-Design<10 circuits/Dispatch InlKY (days)	R&B (POTS) excd SB Or
B.2.1.9.2.1	P-4	2W Analog Loop Non-Design>=10 circuits/Dispatch/KY (days)	R&B (POTS) excd SB Or
B.2.1.9.2.4	P-4	2W Analog Loop Non-Design>=10 circuits/Dispatch InlKY (days)	R&B (POTS) excd SB Or
B.2.1.10.1.1	P-4	2W Analog Loop w/INP Non-Design<10 circuits/Dispatch/KY (days)	R&B (POTS) excd SB Or
B.2.1.10.1.2	P-4	2W Analog Loop w/INP Non-Design<10 circuits/Dispatch InlKY (days)	R&B (POTS) excd SB Or
B.2.1.10.2.1	P-4	2W Analog Loop w/INP Design>=10 circuits/Dispatch/KY (days)	R&B (POTS) excd SB Or
B.2.1.10.2.2	P-4	2W Analog Loop w/INP Design>=10 circuits/Dispatch InlKY (days)	R&B (POTS) excd SB Or
B.2.1.11.1.1	P-4	2W Analog Loop w/INP Non-Design<10 circuits/Dispatch/KY (days)	R&B (POTS) excd SB Or
B.2.1.11.1.4	P-4	2W Analog Loop w/INP Non-Design<10 circuits/Dispatch InlKY (days)	R&B (POTS) excd SB Or
B.2.1.11.2.1	P-4	2W Analog Loop w/INP Non-Design>=10 circuits/Dispatch/KY (days)	R&B (POTS) excd SB Or
B.2.1.11.2.4	P-4	2W Analog Loop w/INP Non-Design>=10 circuits/Dispatch InlKY (days)	R&B (POTS) excd SB Or
B.2.1.12.1.1	P-4	2W Analog Loop w/INP Non-Design<10 circuits/Dispatch/KY (days)	R&B (POTS) excd SB Or
B.2.1.12.1.2	P-4	2W Analog Loop w/INP Non-Design<10 circuits/Dispatch InlKY (days)	R&B (POTS) excd SB Or
B.2.1.12.2.1	P-4	2W Analog Loop w/INP Design>=10 circuits/Dispatch/KY (days)	R&B (POTS) excd SB Or
B.2.1.12.2.2	P-4	2W Analog Loop w/INP Non-Design>=10 circuits/Dispatch InlKY (days)	R&B (POTS) excd SB Or
B.2.1.13.1.1	P-4	2W Analog Loop w/INP Non-Design<10 circuits/Dispatch/KY (days)	R&B (POTS) excd SB Or
B.2.1.13.1.4	P-4	2W Analog Loop w/INP Non-Design<10 circuits/Dispatch InlKY (days)	R&B (POTS) excd SB Or
B.2.1.13.2.1	P-4	2W Analog Loop w/INP Non-Design>=10 circuits/Dispatch/KY (days)	R&B (POTS) excd SB Or
B.2.1.13.2.4	P-4	2W Analog Loop w/INP Non-Design>=10 circuits/Dispatch InlKY (days)	R&B (POTS) excd SB Or
R.2.1.14.1.1	P-4	Other Design/<10 circuits/Dispatch/KY (days)	Design
B.2.1.14.1.2	P-4	Other Design/<10 circuits/Non-Dispatch/KY (days)	Design
B.2.1.14.2.1	P-4	Other Design/>=10 circuits/Dispatch/KY (days)	Design
B.2.1.14.2.2	P-4	Other Design/>=10 circuits/Non-Dispatch/KY (days)	Design

BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
1.02	115,699			1.499			
11.24	21			8.086			
16.09	269			9.561			
7.48	5,540	5.20	167	10.713	0.84142	2.7106	YES
1.02	115,981	0.83	4,914	1.530	0.02228	8.3067	YES
0.33	59,064	0.33	2,799	0.007	0.00013	-0.4879	YES
1.73	56,797	1.50	2,115	1.944	0.04305	5.4791	YES
10.04	28			7.466			
1.44	2	3.00	2	1.068	0.87206	-1.7838	NO
0.33	2			0.000			
2.00	4	3.00	2	0.816	0.70711	-1.4142	YES
8.40	5,983	15.75	4	11.879	5.94154	-1.2370	YES
17.58	73			14.440			
17.58	73			14.440			
13.22	45			6.878			
3.08	17			7.518			
7.00	1			0.000			
2.76	876	4.50	2	1.873	1.32578	-1.3137	YES
2.29	1,831	3.17	12	0.659	0.19091	-4.5939	NO
3.00	1			0.000			
6.00	1			0.000			
7.48	5,540	4.50	4	10.713	5.35857	0.5558	YES
7.48	5,540			10.713			
10.04	28			7.466			
10.04	28			7.466			
7.54	5,440	3.44	9	7.466	3.60106	1.1380	YES
1.73	56,696			10.794	1.896		
11.24	21			8.086			
7.48	5,540			10.713			
7.48	5,540			10.713			
10.04	28			7.466			
10.04	28			7.466			
7.54	5,440			10.794			
1.73	56,696			1.896			
11.24	21			8.086			
7.48	5,540			10.713			
7.48	5,540			10.713			
10.04	28			7.466			
10.04	28			7.466			
7.54	5,440			10.794			
1.73	56,696			1.896			
11.24	21			8.086			
13.33	443			18.099			
11.40	75			8.494			
22.27	45			15.759			
22.17	6			29.404			

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B.2.3.16.2.1	P-1	INP (Standalone)/>=10 circuits/Equipment/KY (days)	R&B (POTS)
B.2.3.16.2.2	P-1	INP (Standalone)/>=10 circuits/Equipment/KY (days)	R&B (POTS)
B.2.3.16.2.3	P-1	INP (Standalone)/>=10 circuits/Other/KY (days)	R&B (POTS)
B.2.3.17.1.1	P-1	LNP (Standalone)/<10 circuits/Equipment/KY (days)	R&B (POTS)
B.2.3.17.1.2	P-1	LNP (Standalone)/<10 circuits/Other/KY (days)	R&B (POTS)
B.2.3.17.1.3	P-1	LNP (Standalone)/>=10 circuits/Equipment/KY (days)	R&B (POTS)
B.2.3.17.2.1	P-1	LNP (Standalone)/>=10 circuits/Equipment/KY (days)	R&B (POTS)
B.2.3.17.2.2	P-1	LNP (Standalone)/>=10 circuits/Other/KY (days)	R&B (POTS)
B.2.3.17.2.3	P-1	LNP (Standalone)/>=10 circuits/Equipment/KY (days)	R&B (POTS)
B.2.3.18.1.1	P-1	Digital Loop < DS1/<10 circuits/Equipment/KY (days)	Digital Loop < DS1
B.2.3.18.1.2	P-1	Digital Loop < DS1/<10 circuits/Equipment/KY (days)	Digital Loop < DS1
B.2.3.18.1.3	P-1	Digital Loop < DS1/<10 circuits/Other/KY (days)	Digital Loop < DS1
B.2.3.19.1.1	P-1	Digital Loop >= DS1/>=10 circuits/Equipment/KY (days)	Digital Loop >= DS1
B.2.3.19.1.2	P-1	Digital Loop >= DS1/>=10 circuits/Other/KY (days)	Digital Loop >= DS1
B.2.3.19.1.3	P-1	Digital Loop >= DS1/>=10 circuits/Equipment/KY (days)	Digital Loop >= DS1
B.2.3.19.2.1	P-1	Digital Loop >= DS1/>=10 circuits/Other/KY (days)	Digital Loop >= DS1
B.2.3.19.2.2	P-1	Digital Loop >= DS1/>=10 circuits/Equipment/KY (days)	Digital Loop >= DS1
B.2.3.19.2.3	P-1	Digital Loop >= DS1/>=10 circuits/Other/KY (days)	Digital Loop >= DS1

% Jeopardies - Mechanized

B.2.5.1	P-2	Switch Ports/KY (%)	R&B (POTS)
B.2.5.2	P-2	Local Interface Transports/KY (%)	DS1/DS3 - Interface
B.2.5.3	P-2	Loop + Port Combinations/KY (%)	R&B
B.2.5.4	P-2	Combo Other/KY (%)	R&B&D - Disp
B.2.5.5	P-2	XDSL (ADSL, HDSL and UCL)/KY (%)	ADSL to Retail
B.2.5.6	P-2	UNE (SDN)/KY (%)	ISDN - BRI
B.2.5.7	P-2	Line Sharing/KY (%)	ADSL to Retail
B.2.5.8	P-2	2W Analog Loop Design/KY (%)	R&B - Disp
B.2.5.9	P-2	2W Analog Loop Non-Design/KY (%)	R&B (POTS) excl SB OR
B.2.5.10	P-2	2W Analog Loop w/INP Design/KY (%)	R&B - Disp
B.2.5.11	P-2	2W Analog Loop w/INP Non-Design/KY (%)	R&B (POTS) excl SB OR
B.2.5.12	P-2	2W Analog Loop w/LNP Design/KY (%)	R&B - Disp
B.2.5.13	P-2	2W Analog Loop w/LNP Non-Design/KY (%)	R&B (POTS) excl SB OR
B.2.5.14	P-2	Other Design/KY (%)	Design
B.2.5.15	P-2	Other Non-Design/KY (%)	R&B
B.2.5.16	P-2	INP (Standalone)/KY (%)	R&B (POTS)
B.2.5.17	P-2	LNP (Standalone)/KY (%)	R&B (POTS)
B.2.5.18	P-2	Digital Loop < DS1/KY (%)	Digital Loop < DS1
B.2.5.19	P-2	Digital Loop >= DS1/KY (%)	Digital Loop >= DS1

% Jeopardies - Non-Mechanized

B.2.6.1	P-2	Switch Ports/KY (%)	Diagnostic
B.2.6.2	P-2	Local Interface Transports/KY (%)	Diagnostic
B.2.6.3	P-2	Loop + Port Combinations/KY (%)	Diagnostic
B.2.6.4	P-2	Combo Other/KY (%)	Diagnostic
B.2.6.5	P-2	XDSL (ADSL, HDSL and UCL)/KY (%)	Diagnostic
B.2.6.6	P-2	UNE (SDN)/KY (%)	Diagnostic
B.2.6.7	P-2	Line Sharing/KY (%)	Diagnostic
B.2.6.8	P-2	2W Analog Loop Design/KY (%)	Diagnostic
B.2.6.9	P-2	2W Analog Loop Non-Design/KY (%)	Diagnostic
B.2.6.10	P-2	2W Analog Loop w/INP Design/KY (%)	Diagnostic
B.2.6.11	P-2	2W Analog Loop w/INP Non-Design/KY (%)	Diagnostic
B.2.6.12	P-2	2W Analog Loop w/LNP Design/KY (%)	Diagnostic
B.2.6.13	P-2	2W Analog Loop w/LNP Non-Design/KY (%)	Diagnostic
B.2.6.14	P-2	Other Design/KY (%)	Diagnostic
B.2.6.15	P-2	Other Non-Design/KY (%)	Diagnostic
B.2.6.16	P-2	INP (Standalone)/KY (%)	Diagnostic

Benchmark/Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
R&B (POTS)	0.00	0						
R&B (POTS)	0.00	0						
R&B (POTS)	0.00	0	0.00	0	64.594	0.00064	4.3042	YES
R&B (POTS)	41.31	131	0.00	0				YES
R&B (POTS)	0.00	0	0.00	0				YES
R&B (POTS)	8.23	31	0.00	0	14.315	0.00946	4.5963	NO
R&B (POTS)	0.00	0	0.00	0				YES
R&B (POTS)	0.00	0	0.00	0				YES
R&B (POTS)	0.00	0	0.00	0				YES
R&B (POTS)	68.00	3	0.00	0	89.772	0.02500	0.2769	YES
Digital Loop < DS1	0.00	0	0.00	0				YES
Digital Loop < DS1	0.00	0	0.00	0				YES
Digital Loop < DS1	0.00	0	0.00	0				YES
Digital Loop < DS1	0.00	0	0.00	0				YES
Digital Loop >= DS1	0.00	0	0.00	0				YES
Digital Loop >= DS1	1.00	1	0.00	0	0.000	0.00954	-0.1366	YES
Digital Loop >= DS1	0.00	0						
Digital Loop >= DS1	0.00	0						
Digital Loop >= DS1	0.00	0						

R&B (POTS)	0.38%	130,073						
DS1/DS3 - Interface	69.47%	285	0.11%	10,064				
R&B	0.39%	130,353						YES
R&B&D - Disp	8.25%	6,986	0.00%	7	0.10788	0.00064	4.3042	YES
ADSL to Retail	1.41%	2,869	33.33%	3	0.06946	0.00946	4.5963	NO
ISDN - BRI	8.92%	2,869	0.00%	1	0.28512	0.28512	0.3129	YES
ADSL to Retail	0.39%	130,353						YES
R&B - Disp	0.69%	71,063	0.00%	11	0.02500	0.02500	0.2769	YES
R&B (POTS) excl SB OR	0.39%	130,353						
R&B - Disp	0.69%	71,063						
R&B (POTS) excl SB OR	0.38%	130,073						YES
R&B (POTS)	0.38%	130,073	0.00%	1,715	0.00149	0.00149	2.6351	YES
Digital Loop < DS1	8.76%	3,071	10.00%	10	0.08954	0.08954	-0.1366	YES
Digital Loop >= DS1	22.44%	156	72.73%	22	0.09500	0.09500	-5.2936	NO

Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic	0.97%	207						Diagnostic
Diagnostic	44.44%	9						Diagnostic
Diagnostic								Diagnostic
Diagnostic	0.00%	45						Diagnostic
Diagnostic	0.00%	4						Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic
Diagnostic	0.00%	1						Diagnostic
Diagnostic								Diagnostic
Diagnostic	0.00%	1						Diagnostic
Diagnostic								Diagnostic
Diagnostic								Diagnostic

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B.2.18.1.2	P-3	Switch Ports<=10 circuits/DispatchKY (%)
B.2.18.1.2.1	P-3	Switch Ports>=10 circuits/DispatchKY (%)
B.2.18.1.2.2	P-3	Switch Ports<=10 circuits/Non-DispatchKY (%)
B.2.18.2.1	P-3	Local Interface Transport<10 circuits/DispatchKY (%)
B.2.18.2.1.2	P-3	Local Interface Transport<10 circuits/Non-DispatchKY (%)
B.2.18.2.2	P-3	Local Interface Transport>=10 circuits/DispatchKY (%)
B.2.18.2.2.2	P-3	Local Interface Transport>=10 circuits/Non-DispatchKY (%)
B.2.18.3.1	P-3	Loop + Port Combinations<10 circuits/DispatchKY (%)
B.2.18.3.1.2	P-3	Loop + Port Combinations<10 circuits/Non-DispatchKY (%)
B.2.18.3.1.3	P-3	Loop + Port Combinations<10 circuits/Switch Based OrdersKY (%)
B.2.18.3.1.4	P-3	Loop + Port Combinations<10 circuits/Dispatch InKY (%)
B.2.18.3.2	P-3	Loop + Port Combinations>=10 circuits/DispatchKY (%)
B.2.18.3.2.3	P-3	Loop + Port Combinations>=10 circuits/Switch Based OrdersKY (%)
B.2.18.3.2.4	P-3	Loop + Port Combinations>=10 circuits/Dispatch InKY (%)
B.2.18.4.1	P-3	Combo Other<10 circuits/Dispatch InKY (%)
B.2.18.4.1.4	P-3	Combo Other/>=10 circuits/Dispatch InKY (%)
B.2.18.4.2.1	P-3	Combo Other/>=10 circuits/Dispatch InKY (%)
B.2.18.4.2.4	P-3	Combo Other/>=10 circuits/Dispatch InKY (%)
B.2.18.5.1	P-3	XDSL (ADSL, HDSL and UCL) <10 circuits/DispatchKY (%)
B.2.18.5.1.2	P-3	XDSL (ADSL, HDSL and UCL) <10 circuits/Non-DispatchKY (%)
B.2.18.5.2.1	P-3	XDSL (ADSL, HDSL and UCL) >=10 circuits/DispatchKY (%)
B.2.18.5.2.2	P-3	XDSL (ADSL, HDSL and UCL) >=10 circuits/Non-DispatchKY (%)
B.2.18.6.1.1	P-3	UNE ISDN/>=10 circuits/Non-DispatchKY (%)
B.2.18.6.1.2	P-3	UNE ISDN/>=10 circuits/DispatchKY (%)
B.2.18.6.2.1	P-3	UNE ISDN/>=10 circuits/Non-DispatchKY (%)
B.2.18.6.2.2	P-3	UNE ISDN/>=10 circuits/DispatchKY (%)
B.2.18.7.1.1	P-3	Line Sharing<10 circuits/DispatchKY (%)
B.2.18.7.1.2	P-3	Line Sharing<10 circuits/Non-DispatchKY (%)
B.2.18.7.2.1	P-3	Line Sharing>=10 circuits/DispatchKY (%)
B.2.18.7.2.2	P-3	Line Sharing>=10 circuits/Non-DispatchKY (%)
B.2.18.8.1.1	P-3	2W Analog Loop Design<10 circuits/DispatchKY (%)
B.2.18.8.1.2	P-3	2W Analog Loop Design<10 circuits/Non-DispatchKY (%)
B.2.18.8.2.1	P-3	2W Analog Loop Design>=10 circuits/DispatchKY (%)
B.2.18.8.2.2	P-3	2W Analog Loop Design>=10 circuits/Non-DispatchKY (%)
B.2.18.9.1.1	P-3	2W Analog Loop Non-Design<10 circuits/DispatchKY (%)
B.2.18.9.1.4	P-3	2W Analog Loop Non-Design<10 circuits/Non-DispatchKY (%)
B.2.18.9.2.1	P-3	2W Analog Loop Non-Design>=10 circuits/DispatchKY (%)
B.2.18.9.2.4	P-3	2W Analog Loop Non-Design>=10 circuits/Non-DispatchKY (%)
B.2.18.10.1.1	P-3	2W Analog Loop w/INP Design<10 circuits/DispatchKY (%)
B.2.18.10.1.2	P-3	2W Analog Loop w/INP Design<10 circuits/Non-DispatchKY (%)
B.2.18.10.2.1	P-3	2W Analog Loop w/INP Design>=10 circuits/DispatchKY (%)
B.2.18.10.2.2	P-3	2W Analog Loop w/INP Design>=10 circuits/Non-DispatchKY (%)
B.2.18.11.1	P-3	2W Analog Loop w/INP Non-Design<10 circuits/DispatchKY (%)
B.2.18.11.1.4	P-3	2W Analog Loop w/INP Non-Design<10 circuits/Non-DispatchKY (%)
B.2.18.11.2.1	P-3	2W Analog Loop w/INP Non-Design>=10 circuits/DispatchKY (%)
B.2.18.11.2.4	P-3	2W Analog Loop w/INP Non-Design>=10 circuits/Non-DispatchKY (%)
B.2.18.12.1.1	P-12	2W Analog Loop w/INP Design<10 circuits/DispatchKY (%)
B.2.18.12.1.2	P-12	2W Analog Loop w/INP Design<10 circuits/Non-DispatchKY (%)
B.2.18.12.2.1	P-12	2W Analog Loop w/INP Design>=10 circuits/DispatchKY (%)
B.2.18.12.2.2	P-12	2W Analog Loop w/INP Design>=10 circuits/Non-DispatchKY (%)
B.2.18.13.1	P-12	2W Analog Loop w/INP Non-Design<10 circuits/DispatchKY (%)
B.2.18.13.1.4	P-12	2W Analog Loop w/INP Non-Design<10 circuits/Non-DispatchKY (%)
B.2.18.13.2.1	P-12	2W Analog Loop w/INP Non-Design>=10 circuits/DispatchKY (%)
B.2.18.13.2.4	P-12	2W Analog Loop w/INP Non-Design>=10 circuits/Non-DispatchKY (%)
B.2.18.14.1.1	P-3	Other Design<10 circuits/DispatchKY (%)
B.2.18.14.1.2	P-3	Other Design<10 circuits/Non-DispatchKY (%)
B.2.18.14.2.1	P-3	Other Design>=10 circuits/DispatchKY (%)
B.2.18.14.2.2	P-3	Other Design>=10 circuits/Non-DispatchKY (%)

Benchmark/Analogs	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
R&B (POTS)	0.03%	123,865						
R&B (POTS)	10.34%	29						
R&B (POTS)	1.49%	269						
DS1/DS3								
DS1/DS3								
DS1/DS3								
R&B	14.05%	6,283	7.73%	233		0.02319	2.7294	YES
R&B	0.03%	124,027	0.02%	10,166		0.00019	0.6817	YES
R&B	0.00%	59,087	0.00%	2,812		0.00000		YES
R&B	0.06%	64,930	0.03%	7,374		0.00030	1.1309	YES
R&B	0.00%	6	0.00%	1		0.28020	0.2974	YES
R&B	0.00%	2		5		0.00000		YES
R&B	0.00%	4	0.00%	5		0.00000		YES
R&B	13.58%	6,752	0.00%	9		0.11427	1.1885	YES
R&B	13.58%	6,752						
R&B	3.88%	103						
R&B	3.88%	103						
R&B	7.95%	1,069	0.00%	5		0.12127	0.6557	YES
R&B	0.16%	1,853						
R&B	0.00%	1						
R&B	6.00%	50	0.00%	1		0.23985	0.2502	YES
R&B	0.00%	22						
ISDN - BRI								
ISDN - BRI								
ISDN - BRI								
ADSL to Retail	7.95%	1,069	33.33%	3		0.15641	-1.6227	YES
ADSL to Retail	0.16%	1,853	2.33%	43		0.00620	-3.4888	NO
ADSL to Retail	0.00%	1						
R&B - Disp	14.05%	6,283	0.00%	4		0.17383	0.8085	YES
R&B - Disp	14.05%	6,283						
R&B - Disp	8.33%	36						
R&B - Disp	8.33%	36						
R&B - Disp	14.24%	6,180						
R&B (POTS) exc SB Or	0.06%	64,819	0.00%	11		0.10546	1.3502	YES
R&B (POTS) exc SB Or	10.34%	29						
R&B - Disp	14.05%	6,283						
R&B - Disp	14.05%	6,283						
R&B - Disp	8.33%	36						
R&B - Disp	8.33%	36						
R&B (POTS) exc SB Or	14.24%	6,180						
R&B (POTS) exc SB Or	0.06%	64,819	0.00%	1		0.02483	0.0248	YES
R&B (POTS) exc SB Or	10.34%	29						
Design	7.25%	469						
Design	0.00%	77						
Design	1.49%	67						
Design	0.00%	6						

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B.2.18.15.1.1	P-3	Other Non-Design<10 circuits/DispatchKTY (%)	R&B
B.2.18.15.1.2	P-3	Other Non-Design<10 circuits/Non-DispatchKTY (%)	R&B
B.2.18.15.2.1	P-3	Other Non-Design/>=10 circuits/DispatchKTY (%)	R&B
B.2.18.15.2.2	P-3	Other Non-Design/>=10 circuits/Non-DispatchKTY (%)	R&B
B.2.18.16.1.1	P-3	INP (Standalone)<10 circuits/DispatchKTY (%)	R&B (POTS)
B.2.18.16.1.2	P-3	INP (Standalone)<10 circuits/Non-DispatchKTY (%)	R&B (POTS)
B.2.18.16.2.1	P-3	INP (Standalone)/>=10 circuits/DispatchKTY (%)	R&B (POTS)
B.2.18.16.2.2	P-3	INP (Standalone)/>=10 circuits/Non-DispatchKTY (%)	R&B (POTS)
B.2.18.17.1.1	P-12	LNP (Standalone)<10 circuits/DispatchKTY (%)	R&B (POTS)
B.2.18.17.1.2	P-12	LNP (Standalone)<10 circuits/Non-DispatchKTY (%)	R&B (POTS)
B.2.18.17.2.1	P-12	LNP (Standalone)/>=10 circuits/DispatchKTY (%)	R&B (POTS)
B.2.18.17.2.2	P-12	LNP (Standalone)/>=10 circuits/Non-DispatchKTY (%)	R&B (POTS)
B.2.18.18.1.1	P-3	Digital Loop < DS1<10 circuits/DispatchKTY (%)	Digital Loop < DS1
B.2.18.18.1.2	P-3	Digital Loop < DS1/>=10 circuits/DispatchKTY (%)	Digital Loop < DS1
B.2.18.18.2.1	P-3	Digital Loop < DS1/>=10 circuits/Non-DispatchKTY (%)	Digital Loop < DS1
B.2.18.18.2.2	P-3	Digital Loop >= DS1/<10 circuits/DispatchKTY (%)	Digital Loop >= DS1
B.2.18.19.1.1	P-3	Digital Loop >= DS1/<10 circuits/Non-DispatchKTY (%)	Digital Loop >= DS1
B.2.18.19.1.2	P-3	Digital Loop >= DS1/>=10 circuits/DispatchKTY (%)	Digital Loop >= DS1
B.2.18.19.2.1	P-3	Digital Loop >= DS1/>=10 circuits/Non-DispatchKTY (%)	Digital Loop >= DS1

%Provisioning Troubles within 30 Days

B.2.19.1.1.1	P-9	Switch Ports<10 circuits/DispatchKTY (%)	R&B (POTS)
B.2.19.1.1.2	P-9	Switch Ports<10 circuits/Non-DispatchKTY (%)	R&B (POTS)
B.2.19.1.2.1	P-9	Switch Ports/>=10 circuits/DispatchKTY (%)	R&B (POTS)
B.2.19.1.2.2	P-9	Switch Ports/>=10 circuits/Non-DispatchKTY (%)	DS1/DS3
B.2.19.2.1.1	P-9	Local Interface Transport<10 circuits/DispatchKTY (%)	DS1/DS3
B.2.19.2.1.2	P-9	Local Interface Transport/>=10 circuits/DispatchKTY (%)	DS1/DS3
B.2.19.2.2.1	P-9	Local Interface Transport<10 circuits/Non-DispatchKTY (%)	DS1/DS3
B.2.19.2.2.2	P-9	Local Interface Transport/>=10 circuits/Non-DispatchKTY (%)	DS1/DS3
B.2.19.3.1.1	P-9	Loop + Port Combinations<10 circuits/Non-DispatchKTY (%)	R&B
B.2.19.3.1.2	P-9	Loop + Port Combinations/>=10 circuits/Non-DispatchKTY (%)	R&B
B.2.19.3.1.3	P-9	Loop + Port Combinations<10 circuits/Switch Based OrdersKTY (%)	R&B
B.2.19.3.1.4	P-9	Loop + Port Combinations/>=10 circuits/Switch Based OrdersKTY (%)	R&B
B.2.19.3.2.1	P-9	Loop + Port Combinations/>=10 circuits/Non-DispatchKTY (%)	R&B
B.2.19.3.2.2	P-9	Loop + Port Combinations/>=10 circuits/Switch Based OrdersKTY (%)	R&B
B.2.19.3.2.3	P-9	Loop + Port Combinations/>=10 circuits/DispatchKTY (%)	R&B&D - Disp
B.2.19.3.2.4	P-9	Combo Other/<10 circuits/DispatchKTY (%)	R&B&D - Disp
B.2.19.4.1.1	P-9	Combo Other/<10 circuits/DispatchKTY (%)	R&B&D - Disp
B.2.19.4.1.2	P-9	Combo Other/>=10 circuits/DispatchKTY (%)	R&B&D - Disp
B.2.19.4.2.1	P-9	Combo Other/>=10 circuits/Non-DispatchKTY (%)	R&B&D - Disp
B.2.19.4.2.2	P-9	Combo Other/>=10 circuits/Switch Based OrdersKTY (%)	R&B&D - Disp
B.2.19.5.1.1	P-9	XDSL (ADSL, HDSL and UCL)<10 circuits/DispatchKTY (%)	ADSL to Retail
B.2.19.5.1.2	P-9	XDSL (ADSL, HDSL and UCL)<10 circuits/Non-DispatchKTY (%)	ADSL to Retail
B.2.19.5.2.1	P-9	XDSL (ADSL, HDSL and UCL)/>=10 circuits/DispatchKTY (%)	ADSL to Retail
B.2.19.5.2.2	P-9	XDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-DispatchKTY (%)	ADSL to Retail
B.2.19.6.1.1	P-9	UNE ISDN/<10 circuits/DispatchKTY (%)	ISDN - BRI
B.2.19.6.1.2	P-9	UNE ISDN/<10 circuits/Non-DispatchKTY (%)	ISDN - BRI
B.2.19.6.2.1	P-9	UNE ISDN/>=10 circuits/DispatchKTY (%)	ISDN - BRI
B.2.19.6.2.2	P-9	UNE ISDN/>=10 circuits/Non-DispatchKTY (%)	ISDN - BRI
B.2.19.7.1.1	P-9	Line Sharing<10 circuits/DispatchKTY (%)	ADSL to Retail
B.2.19.7.1.2	P-9	Line Sharing<10 circuits/Non-DispatchKTY (%)	ADSL to Retail
B.2.19.7.2.1	P-9	Line Sharing/>=10 circuits/DispatchKTY (%)	ADSL to Retail
B.2.19.7.2.2	P-9	Line Sharing/>=10 circuits/Non-DispatchKTY (%)	ADSL to Retail
B.2.19.8.1.1	P-9	2W Analog Loop Design/<10 circuits/DispatchKTY (%)	R&B - Disp
B.2.19.8.1.2	P-9	2W Analog Loop Design/<10 circuits/Non-DispatchKTY (%)	R&B - Disp
B.2.19.8.2.1	P-9	2W Analog Loop Design/>=10 circuits/DispatchKTY (%)	R&B - Disp
B.2.19.8.2.2	P-9	2W Analog Loop Design/>=10 circuits/Non-DispatchKTY (%)	R&B - Disp
B.2.19.9.1.1	P-9	2W Analog Loop Non-Design/<10 circuits/DispatchKTY (%)	R&B (POTS) excl SB Or
B.2.19.9.1.2	P-9	2W Analog Loop Non-Design/<10 circuits/Non-DispatchKTY (%)	R&B (POTS) excl SB Or

Benchmark/ Analog

B.2.18.15.1.1	R&B
B.2.18.15.1.2	R&B
B.2.18.15.2.1	R&B
B.2.18.15.2.2	R&B
B.2.18.16.1.1	R&B (POTS)
B.2.18.16.1.2	R&B (POTS)
B.2.18.16.2.1	R&B (POTS)
B.2.18.16.2.2	R&B (POTS)
B.2.18.17.1.1	R&B (POTS)
B.2.18.17.1.2	R&B (POTS)
B.2.18.17.2.1	R&B (POTS)
B.2.18.17.2.2	R&B (POTS)
B.2.18.18.1.1	Digital Loop < DS1
B.2.18.18.1.2	Digital Loop < DS1
B.2.18.18.2.1	Digital Loop < DS1
B.2.18.18.2.2	Digital Loop >= DS1
B.2.18.19.1.1	Digital Loop >= DS1
B.2.18.19.1.2	Digital Loop >= DS1
B.2.18.19.2.1	Digital Loop >= DS1

BST Measure

B.2.18.15.1.1	14.05%
B.2.18.15.1.2	0.03%
B.2.18.15.2.1	8.33%
B.2.18.15.2.2	0.00%
B.2.18.16.1.1	14.24%
B.2.18.16.1.2	0.03%
B.2.18.16.2.1	10.34%
B.2.18.16.2.2	14.24%
B.2.18.17.1.1	0.03%
B.2.18.17.1.2	10.34%
B.2.18.17.2.1	7.63%
B.2.18.17.2.2	0.16%
B.2.18.18.1.1	0.00%
B.2.18.18.1.2	0.00%
B.2.18.18.2.1	10.47%
B.2.18.18.2.2	0.00%
B.2.18.19.1.1	6.25%
B.2.18.19.1.2	0.00%

BST Volume

B.2.18.15.1.1	6,283
B.2.18.15.1.2	124,027
B.2.18.15.2.1	36
B.2.18.15.2.2	6
B.2.18.16.1.1	6,180
B.2.18.16.1.2	123,865
B.2.18.16.2.1	29
B.2.18.16.2.2	6,180
B.2.18.17.1.1	123,865
B.2.18.17.1.2	29
B.2.18.17.2.1	1,192
B.2.18.17.2.2	1,935
B.2.18.18.1.1	1
B.2.18.18.1.2	20
B.2.18.18.2.1	86
B.2.18.18.2.2	53
B.2.18.19.1.1	16
B.2.18.19.1.2	3

CLEC Measure

B.2.18.15.1.1	
B.2.18.15.1.2	
B.2.18.15.2.1	
B.2.18.15.2.2	
B.2.18.16.1.1	100.00%
B.2.18.16.1.2	
B.2.18.16.2.1	
B.2.18.16.2.2	
B.2.18.17.1.1	
B.2.18.17.1.2	
B.2.18.17.2.1	0.00%
B.2.18.17.2.2	0.00%
B.2.18.18.1.1	
B.2.18.18.1.2	
B.2.18.18.2.1	0.00%
B.2.18.18.2.2	
B.2.18.19.1.1	
B.2.18.19.1.2	

CLEC Volume

B.2.18.15.1.1	
B.2.18.15.1.2	
B.2.18.15.2.1	
B.2.18.15.2.2	
B.2.18.16.1.1	1
B.2.18.16.1.2	
B.2.18.16.2.1	
B.2.18.16.2.2	
B.2.18.17.1.1	
B.2.18.17.1.2	
B.2.18.17.2.1	4
B.2.18.17.2.2	6
B.2.18.18.1.1	
B.2.18.18.1.2	
B.2.18.18.2.1	20
B.2.18.18.2.2	
B.2.18.19.1.1	
B.2.18.19.1.2	

Standard Deviation

B.2.18.15.1.1	
B.2.18.15.1.2	
B.2.18.15.2.1	
B.2.18.15.2.2	
B.2.18.16.1.1	0.34948
B.2.18.16.1.2	
B.2.18.16.2.1	
B.2.18.16.2.2	
B.2.18.17.1.1	
B.2.18.17.1.2	
B.2.18.17.2.1	0.10868
B.2.18.17.2.2	0.7024
B.2.18.18.1.1	
B.2.18.18.1.2	
B.2.18.18.2.1	0.07589
B.2.18.18.2.2	1.3772
B.2.18.19.1.1	
B.2.18.19.1.2	

Standard Error

B.2.18.15.1.1	
B.2.18.15.1.2	
B.2.18.15.2.1	
B.2.18.15.2.2	
B.2.18.16.1.1	0.25449
B.2.18.16.1.2	
B.2.18.16.2.1	
B.2.18.16.2.2	
B.2.18.17.1.1	
B.2.18.17.1.2	
B.2.18.17.2.1	0.17116
B.2.18.17.2.2	0.7303
B.2.18.18.1.1	
B.2.18.18.1.2	
B.2.18.18.2.1	0.08560
B.2.18.18.2.2	1.2468
B.2.18.19.1.1	
B.2.18.19.1.2	

Zscore

B.2.18.15.1.1	
B.2.18.15.1.2	
B.2.18.15.2.1	
B.2.18.15.2.2	
B.2.18.16.1.1	-2.4539
B.2.18.16.1.2	
B.2.18.16.2.1	
B.2.18.16.2.2	
B.2.18.17.1.1	
B.2.18.17.1.2	
B.2.18.17.2.1	
B.2.18.17.2.2	
B.2.18.18.1.1	
B.2.18.18.1.2	
B.2.18.18.2.1	
B.2.18.18.2.2	
B.2.18.19.1.1	
B.2.18.19.1.2	

Equity

B.2.18.15.1.1	
B.2.18.15.1.2	
B.2.18.15.2.1	
B.2.18.15.2.2	
B.2.18.16.1.1	NO
B.2.18.16.1.2	
B.2.18.16.2.1	
B.2.18.16.2.2	
B.2.18.17.1.1	
B.2.18.17.1.2	
B.2.18.17.2.1	
B.2.18.17.2.2	
B.2.18.18.1.1	
B.2.18.18.1.2	
B.2.18.18.2.1	YES
B.2.18.18.2.2	
B.2.18.19.1.1	
B.2.18.19.1.2	

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B3.2.9.1	M&R-2	2W Analog Loop Non-Design/DispatchKY (%)	
B3.2.9.2	M&R-2	2W Analog Loop Non-Design/DispatchKY (%)	
B3.2.10.1	M&R-2	Other Design/DispatchKY (%)	
B3.2.10.2	M&R-2	Other Design/DispatchKY (%)	
B3.2.11.1	M&R-2	Other Non-Design/DispatchKY (%)	
B3.2.11.2	M&R-2	Other Non-Design/DispatchKY (%)	
B3.2.12.1	M&R-2	LNP (Standalone)/Non-DispatchKY (%)	
B3.2.12.2	M&R-2	LNP (Standalone)/Non-DispatchKY (%)	

Benchmark/ Analog	R&B (POTS) excl SB FT	R&B (POTS) excl SB FT
	Design	Design
	R&B	R&B
	R&B (POTS)	R&B (POTS)
	R&B (POTS)	R&B (POTS)

BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
1.81%	998,836	0.84%	237	35,265	0.00874	1.1059	YES
0.64%	998,836	0.00%	237	4,046	0.00520	-1.2329	YES
0.26%	161,535	2.84%	915	18,408	0.00169	-15.3149	NO
0.29%	161,535	0.77%	915	18,408	0.00179	-2.6573	NO
1.71%	1,073,486	0.00%	158	35,262	0.01039	1.6415	YES
0.68%	1,073,486	0.00%	158	64,178	0.00654	1.0334	YES
1.82%	998,836						
0.72%	998,836						

B3.3.1.1	M&R-3	Switch Ports/DispatchKY (hours)	
B3.3.1.2	M&R-3	Switch Ports/Non-DispatchKY (hours)	
B3.3.2.1	M&R-3	Local Interface Transport/DispatchKY (hours)	
B3.3.2.2	M&R-3	Local Interface Transport/Non-DispatchKY (hours)	
B3.3.3.1	M&R-3	Loop + Port Combination/DispatchKY (hours)	
B3.3.3.2	M&R-3	Loop + Port Combination/Non-DispatchKY (hours)	
B3.3.4.1	M&R-3	Combo Other/DispatchKY (hours)	
B3.3.4.2	M&R-3	Combo Other/Non-DispatchKY (hours)	
B3.3.5.1	M&R-3	XDSL (ADSL, HDSL, and UCL)/DispatchKY (hours)	
B3.3.5.2	M&R-3	XDSL (ADSL, HDSL, and UCL)/Non-DispatchKY (hours)	
B3.3.6.1	M&R-3	UNE ISDN/DispatchKY (hours)	
B3.3.6.2	M&R-3	UNE ISDN/Non-DispatchKY (hours)	
B3.3.7.1	M&R-3	Line Sharing/DispatchKY (hours)	
B3.3.7.2	M&R-3	Line Sharing/Non-DispatchKY (hours)	
B3.3.8.1	M&R-3	2W Analog Loop Design/DispatchKY (hours)	
B3.3.8.2	M&R-3	2W Analog Loop Design/Non-DispatchKY (hours)	
B3.3.9.1	M&R-3	2W Analog Loop Non-Design/DispatchKY (hours)	
B3.3.9.2	M&R-3	2W Analog Loop Non-Design/Non-DispatchKY (hours)	
B3.3.10.1	M&R-3	Other Design/DispatchKY (hours)	
B3.3.10.2	M&R-3	Other Design/Non-DispatchKY (hours)	
B3.3.11.1	M&R-3	Other Non-Design/DispatchKY (hours)	
B3.3.11.2	M&R-3	Other Non-Design/Non-DispatchKY (hours)	
B3.3.12.1	M&R-3	LNP (Standalone)/DispatchKY (hours)	
B3.3.12.2	M&R-3	LNP (Standalone)/Non-DispatchKY (hours)	

R&B (POTS)	R&B (POTS)
DS1/DS3	DS1/DS3
R&B	R&B
R&B&D - Disp	R&B&D - Disp
ADSL to Retail	ADSL to Retail
ADSL to Retail	ADSL to Retail
ISDN - BRI	ISDN - BRI
ISDN - BRI	ISDN - BRI
ADSL to Retail	ADSL to Retail
ADSL to Retail	ADSL to Retail
R&B - Disp	R&B - Disp
R&B (POTS) excl SB FT	R&B (POTS) excl SB FT
Design	Design
R&B	R&B
R&B (POTS)	R&B (POTS)
R&B (POTS)	R&B (POTS)

26.79	18,142			35,265			
9.26	7,161	0.00	0	18,467			
5.11	130	0.00	0	4,046			YES
1.95	94	0.00	0	2,125			YES
26.70	18,310	18.73	924	35,510			YES
9.20	7,297	4.82	204	18,408			YES
26.21	18,728	4.84	3	35,262			YES
26.21	18,728	3.06	6	64,178			YES
42.66	340	3.42	1	64,178			YES
4.77	661	4.98	2	23,631			YES
5.39	12	1.87	4	3,992			YES
8.24	17	3.34	3	12,182			YES
42.66	340	28.33	6	64,178			YES
4.77	661	15.00	9	23,631			YES
26.70	18,310	5.49	2	35,510			YES
26.70	18,310	0.25	1	35,510			YES
9.92	6,408	0.00	0	19,174			YES
5.01	468	4.57	26	3,918			YES
2.81	419	1.85	7	4,652			YES
26.70	18,310	0.00	0	35,510			YES
9.20	7,297	0.00	0	18,408			YES
26.79	18,142			35,265			YES
9.26	7,161			18,467			

B3.4.1.1	M&R-4	Switch Ports/DispatchKY (%)	
B3.4.1.2	M&R-4	Switch Ports/Non-DispatchKY (%)	
B3.4.2.1	M&R-4	Local Interface Transport/DispatchKY (%)	
B3.4.2.2	M&R-4	Local Interface Transport/Non-DispatchKY (%)	
B3.4.3.1	M&R-4	Loop + Port Combination/DispatchKY (%)	
B3.4.3.2	M&R-4	Loop + Port Combination/Non-DispatchKY (%)	
B3.4.4.1	M&R-4	Combo Other/DispatchKY (%)	
B3.4.4.2	M&R-4	Combo Other/Non-DispatchKY (%)	
B3.4.5.1	M&R-4	XDSL (ADSL, HDSL, and UCL)/DispatchKY (%)	
B3.4.5.2	M&R-4	XDSL (ADSL, HDSL, and UCL)/Non-DispatchKY (%)	
B3.4.6.1	M&R-4	UNE ISDN/DispatchKY (%)	
B3.4.6.2	M&R-4	UNE ISDN/Non-DispatchKY (%)	
B3.4.7.1	M&R-4	Line Sharing/DispatchKY (%)	
B3.4.7.2	M&R-4	Line Sharing/Non-DispatchKY (%)	
B3.4.8.1	M&R-4	2W Analog Loop Design/DispatchKY (%)	
B3.4.8.2	M&R-4	2W Analog Loop Design/Non-DispatchKY (%)	
B3.4.9.1	M&R-4	2W Analog Loop Non-Design/DispatchKY (%)	
B3.4.9.2	M&R-4	2W Analog Loop Non-Design/Non-DispatchKY (%)	
B3.4.10.1	M&R-4	Other Design/DispatchKY (%)	
B3.4.10.2	M&R-4	Other Design/Non-DispatchKY (%)	
B3.4.11.1	M&R-4	Other Non-Design/DispatchKY (%)	
B3.4.11.2	M&R-4	Other Non-Design/Non-DispatchKY (%)	
B3.4.12.1	M&R-4	LNP (Standalone)/DispatchKY (%)	
B3.4.12.2	M&R-4	LNP (Standalone)/Non-DispatchKY (%)	

R&B (POTS)	R&B (POTS)
DS1/DS3	DS1/DS3
R&B	R&B
R&B&D - Disp	R&B&D - Disp
ADSL to Retail	ADSL to Retail
ADSL to Retail	ADSL to Retail
ISDN - BRI	ISDN - BRI
ISDN - BRI	ISDN - BRI
ADSL to Retail	ADSL to Retail
ADSL to Retail	ADSL to Retail
R&B - Disp	R&B - Disp
R&B (POTS) excl SB FT	R&B (POTS) excl SB FT
Design	Design
R&B	R&B
R&B (POTS)	R&B (POTS)
R&B (POTS)	R&B (POTS)

17.58%	18,142						
12.89%	7,161	0.00%	0				
23.08%	130	0.00%	0				YES
17.02%	94	0.00%	0				YES
17.58%	18,310	12.77%	924				YES
12.91%	7,257	11.76%	204				YES
17.65%	18,728	50.00%	6				NO
17.65%	18,728	66.67%	3				NO
21.65%	340	0.00%	1				YES
23.30%	661	0.00%	2				YES
26.00%	12	0.00%	4				YES
29.41%	17	33.33%	3				YES
27.65%	340	16.67%	6				YES
23.30%	661	0.00%	2				YES
17.58%	18,310	11.11%	9				YES
17.58%	18,310	0.00%	1				YES
17.65%	18,085	0.00%	2				YES
13.09%	6,408	0.00%	0				YES
20.81%	418	23.08%	26				YES
17.91%	469	14.29%	7				YES
17.55%	16,310	0.05%	0				YES
12.91%	7,257	0.00%	0				YES
17.58%	18,142						YES
12.89%	7,161						YES

BellSouth Monthly State Summary Kentucky, December 2002

Out of Service > 24 hours		Benchmark/ Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.3.5.1.1	M&R-5 Switch Ports/DispatchKY (%)	R&B (POTS)	14.21%	11,564	0.00%	0				
B.3.5.1.2	M&R-5 Switch Ports/Non-DispatchKY (%)	R&B (POTS)	6.49%	2,188	0.00%	0				YES
B.3.5.2.1	M&R-5 Local Interface Transport/DispatchKY (%)	DS1/DS3	0.77%	130	0.00%	0				YES
B.3.5.2.2	M&R-5 Local Interface Transport/Non-DispatchKY (%)	DS1/DS3	0.00%	94	0.00%	0				YES
B.3.5.3.1	M&R-5 Loop + Port Combination/Non-DispatchKY (%)	R&B	14.15%	11,664	10.15%	650			0.01405	2.8479
B.3.5.3.2	M&R-5 Loop + Port Combination/Non-DispatchKY (%)	R&B	6.50%	2,215	5.08%	118			0.02329	0.6081
B.3.5.4.1	M&R-5 Combo Other/Non-DispatchKY (%)	R&B&D - Disp	13.67%	12,082	0.00%	6			0.14029	0.9746
B.3.5.4.2	M&R-5 Combo Other/Non-DispatchKY (%)	R&B&D - Disp	13.67%	12,082	0.00%	3			0.19838	0.6892
B.3.5.5.1	M&R-5 ADSL (ADSL, HDSL and UCL)/DispatchKY (%)	ADSL to Retail	45.88%	340	0.00%	1			0.45903	0.9194
B.3.5.5.2	M&R-5 ADSL (ADSL, HDSL and UCL)/Non-DispatchKY (%)	ADSL to Retail	4.69%	661	0.00%	2			0.14972	0.3132
B.3.5.6.1	M&R-5 UNE ISDN/DispatchKY (%)	ISDN - BRI	0.00%	12	0.00%	4			0.00000	
B.3.5.6.2	M&R-5 UNE ISDN/Non-DispatchKY (%)	ISDN - BRI	0.00%	17	0.00%	3			0.14735	0.3992
B.3.5.7.1	M&R-5 Line Shaming/DispatchKY (%)	ADSL to Retail	45.88%	340	0.00%	0				
B.3.5.7.2	M&R-5 Line Shaming/Non-DispatchKY (%)	ADSL to Retail	4.69%	661	0.00%	0			0.11624	1.2177
B.3.5.8.1	M&R-5 2W Analog Loop Design/DispatchKY (%)	R&B - Disp	14.15%	11,664	0.00%	9				YES
B.3.5.8.2	M&R-5 2W Analog Loop Design/Non-DispatchKY (%)	R&B - Disp	14.15%	11,664	0.00%	1			0.34860	0.4060
B.3.5.9.1	M&R-5 2W Analog Loop Non-Design/DispatchKY (%)	R&B (POTS) exd SB FT	6.49%	2,187	0.00%	0				YES
B.3.5.9.2	M&R-5 2W Analog Loop Non-Design/Non-DispatchKY (%)	R&B (POTS) exd SB FT	0.24%	418	0.00%	26			0.00987	0.2423
B.3.5.10.1	M&R-5 Other Design/DispatchKY (%)	Design	0.64%	469	0.00%	7			0.03036	0.2107
B.3.5.10.2	M&R-5 Other Design/Non-DispatchKY (%)	Design	14.15%	11,664	0.00%	0				YES
B.3.5.11.1	M&R-5 Other Non-Design/DispatchKY (%)	R&B	6.50%	2,215	0.00%	0				YES
B.3.5.11.2	M&R-5 Other Non-Design/Non-DispatchKY (%)	R&B	14.21%	11,564	0.00%	0				YES
B.3.5.12.1	M&R-5 LNP (Standalone)/DispatchKY (%)	R&B (POTS)	6.49%	2,188						
B.3.5.12.2	M&R-5 LNP (Standalone)/Non-DispatchKY (%)	R&B (POTS)								

Unbundled Network Elements - Billing

Invoice Accuracy	BST - State	BST - Region
B.4.1	99.22%	\$97,330,611
B.4.2	99.22%	\$2,381,043
	4.14	1
	2.91	1,894
	0.00006	0.1264
	0.00006	0.1264
	YES	YES

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		Benchmark/ Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
Maintenance Average Duration										
C.3.3.1	M&R-3	local Interconnection Trunks/DispatchKY (hours)	0.00	0	0.00	0	0.757	0.17860	1.9422	YES
C.3.3.2	M&R-3	local Interconnection Trunks/Non-DispatchKY (hours)	0.40	64	0.05	25				YES
% Repeat Troubles within 30 Days										
C.3.4.1	M&R-4	local Interconnection Trunks/DispatchKY (%)	0.00%	0	0.00%	0		0.00000		YES
C.3.4.2	M&R-4	local Interconnection Trunks/Non-DispatchKY (%)	0.00%	64	0.00%	25				YES
Out of Service > 24 hours										
C.3.5.1	M&R-5	local Interconnection Trunks/DispatchKY (%)	0.00%	0	0.00%	0		0.00000		YES
C.3.5.2	M&R-5	local Interconnection Trunks/Non-DispatchKY (%)	0.00%	64	0.00%	25				YES
Local Interconnection Trunks - Billing										
Invoice Accuracy										
C.4.1	B-1	KY (%)	99.22%	\$97,330,611	96.61%	\$477,163		0.00013	204.6096	NO
Mean Time to Deliver Invoices - CABS										
C.4.2	B-2	Region (calendar days)	5.43	1	5.48	13.010				NO
Local Interconnection Trunks - Trunk Blocking										
Trunk Group Performance - Aggregate										
C.5.1	TRP-1	Local Interconnection Trunks/KY (%)	>0.5% diff 2 consec. hrs			1				NO

Bellsouth Monthly State Summary

Kentucky, December 2002

Benchmark/ Analog BST Measure BST Volume CLEC Measure CLEC Volume Standard Deviation Standard Error Zscore Equity

Operations Support Systems - Pre-Ordering

% Interface Availability - CLEC	
OSS-2	EDR/Region (%)
OSS-2	LENS/Region (%)
OSS-2	LEO/Region (%)
OSS-2	LEOGR/Region (%)
OSS-2	TAG/Region (%)
OSS-2	PSMIS/Region (%)
OSS-2	LNP Gateway/Region (%)
OSS-2	SGC/COG/Region (%)
OSS-2	DCM/Region (%)
OSS-2	SCG/Region (%)

>= 99.5%
>= 99.5%
>= 99.5%
>= 99.5%
>= 99.5%
>= 99.5%
>= 99.5%
>= 99.5%
>= 99.5%
>= 99.5%

99.42%	NO
99.97%	YES
100.00%	YES
100.00%	YES
99.99%	YES
100.00%	YES
99.99%	YES
99.96%	YES
100.00%	YES
100.00%	YES

% Interface Availability - BST & CLEC

OSS-2	ATLAS/COF/Region (%)
OSS-2	COF/Region (%)
OSS-2	BOCRIS/CRIS/Region (%)
OSS-2	DSAP/Region (%)
OSS-2	RSAG/Region (%)
OSS-2	SONGS/Region (%)
OSS-2	DOE/Region (%)

>= 99.5%
>= 99.5%
>= 99.5%
>= 99.5%
>= 99.5%
>= 99.5%
>= 99.5%

100.00%	YES
100.00%	YES
100.00%	YES
100.00%	YES
100.00%	YES
100.00%	YES
100.00%	YES

Average Response Interval - CLEC (LENS) (BST Measure Includes Additional 2 Seconds)

OSS-1	RSAG, by TN/Region (seconds)
OSS-1	RSAG, by ADDR/Region (seconds)
OSS-1	RSAG, by ADDR/Region (seconds)
OSS-1	ATLAS/Region (seconds)
OSS-1	ATLAS/Region (seconds)
OSS-1	DSAP/Region (seconds)
OSS-1	CRSECSRU/Region (seconds)
OSS-1	CRSECSRU/Region (seconds)
OSS-1	COF/Region (seconds)
OSS-1	PSMIS/ORB/Region (seconds)
OSS-1	PSMIS/ORB/Region (seconds)

RNS - RSAG, by TN + 2 sec
ROS - RSAG, by TN + 2 sec
RNS - RSAG, by ADDR + 2 sec
ROS - RSAG, by ADDR + 2 sec
RNS - ATLAS + 2 sec
ROS - ATLAS + 2 sec
RNS - DSAP + 2 sec
ROS - DSAP + 2 sec
RNS - CRSECSRU + 2 sec
ROS - CRSECSRU + 2 sec
RNS - COF + 2 sec
ROS - COF + 2 sec
RNS - OASISBIG + 2 sec
ROS - OASISBIG + 2 sec
RNS - OASISBIG + 2 sec
ROS - OASISBIG + 2 sec

2.35	1,544,752	1.62	410,935	YES
2.81	8,284	1.62	410,935	YES
2.49	3,934,562	1.08	214,201	YES
5.37	676,184	1.08	214,201	YES
2.55	668,417	1.20	72,575	YES
2.30	253,997	1.20	72,575	YES
2.49	1,237,092	2.14	223,916	YES
2.39	285,197	2.14	223,916	YES
2.91	4,421,193	1.12	854,059	YES
2.47	1,048,115	1.12	854,059	YES
3.11	8,248,706	4.13	17,033	NO
3.64	396,948	4.13	17,033	NO
3.11	8,248,706	0.34	43,611	YES
3.64	586,948	0.34	43,611	YES

Average Response Interval - CLEC (TAG) (BST Measure Includes Additional 2 Seconds)

OSS-1	RSAG, by TN/Region (seconds)
OSS-1	RSAG, by TN/Region (seconds)
OSS-1	RSAG, by ADDR/Region (seconds)
OSS-1	RSAG, by ADDR/Region (seconds)
OSS-1	ATLAS - MLH/Region (seconds)
OSS-1	ATLAS - MLH/Region (seconds)
OSS-1	ATLAS - DID/Region (seconds)
OSS-1	ATLAS - DID/Region (seconds)
OSS-1	ATLAS - TN/Region (seconds)
OSS-1	ATLAS - TN/Region (seconds)
OSS-1	ATLAS - TN/Region (seconds)
OSS-1	DSAP/Region (seconds)
OSS-1	DSAP/Region (seconds)
OSS-1	TAG/Region (seconds)
OSS-1	TAG/Region (seconds)

RNS - RSAG, by TN + 2 sec
ROS - RSAG, by TN + 2 sec
RNS - RSAG, by ADDR + 2 sec
ROS - RSAG, by ADDR + 2 sec
Diagnostic
Diagnostic
Diagnostic
Diagnostic
RNS - ATLAS - TN + 2 sec
ROS - ATLAS - TN + 2 sec
RNS - DSAP + 2 sec
ROS - DSAP + 2 sec
RNS - CRSECSRU + 2 sec
ROS - CRSECSRU + 2 sec

2.35	1,544,752	1.36	296,911	YES
2.81	8,284	1.36	296,911	YES
2.49	3,934,562	2.30	101,999	YES
5.37	676,184	2.30	101,999	YES
		0.74	8	Diagnostic
		0.74	8	Diagnostic
2.55	668,417	1.77	29,511	YES
2.30	253,997	1.77	29,511	YES
2.49	1,237,092	1.01	15,838	YES
2.39	285,197	1.01	15,838	YES
2.91	4,421,193	1.53	1,116,689	YES
2.47	1,048,115	1.53	1,116,689	YES

Operations Support Systems - Maintenance and Repair

BellSouth Monthly State Summary Kentucky, December 2002

	Benchmark/ Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
General - Flow Through									
% Flow Through Service Requests									
F 1.1.1	O-3 Summary/Region (%)	Diagnostic	92.56%	428,681	Diagnostic				Diagnostic
F 1.1.2	O-3 Aggregate/Region (%)	Diagnostic	92.56%	428,681	Diagnostic				Diagnostic
F 1.1.3	O-3 Residence/Region (%)	>= 95%	93.55%	146,132	NO				NO
F 1.1.4	O-3 Business/Region (%)	>= 90%	81.40%	4,226	NO				NO
F 1.1.5	O-3 UNE/Region (%)	>= 85%	92.21%	278,324	YES				YES
% Flow Through Service Requests - Achieved									
F 1.2.1	O-3 Summary/Region (%)	Diagnostic	86.56%	458,400	Diagnostic				Diagnostic
F 1.2.2	O-3 Aggregate/Region (%)	Diagnostic	86.56%	458,400	Diagnostic				Diagnostic
F 1.2.3	O-3 Residence/Region (%)	Diagnostic	87.80%	155,702	Diagnostic				Diagnostic
F 1.2.4	O-3 Business/Region (%)	Diagnostic	62.17%	5,532	Diagnostic				Diagnostic
F 1.2.5	O-3 UNE/Region (%)	Diagnostic	86.36%	297,166	Diagnostic				Diagnostic
% Flow Through Service Requests - LNP									
F 1.3.1	O-3 Summary/Region (%)	>= 85%	82.81%	9,405	NO				NO
F 1.3.2	O-3 Aggregate/Region (%)	>= 85%	82.81%	9,405	NO				NO
F 1.3.3	O-3 Residence/Region (%)	Diagnostic			Diagnostic				Diagnostic
F 1.3.4	O-3 Business/Region (%)	Diagnostic			Diagnostic				Diagnostic
General - Pre-Ordering									
Loop Makeup Inquiry (Manual)									
F 2.1	PO-1 Loops/KY (%)	>= 95% w in 3 bus days	100.00%	1	YES				YES
Loop Makeup Inquiry (Electronic)									
F 2.2	PO-2 Loops/KY (%)	>= 95% w in 1 min	98.73%	367	YES				YES
General - Ordering									
Service Inquiry with Firm Order									
F 3.1.1	O-10 KDSL (ADSL, HDSL and UCL)/KY (%)	>= 95% w in 5 bus days	100.00%	2	YES				YES
F 3.1.4	O-10 Local Interior/Transport/KY (%)	>= 95% w in 5 bus days							
Average Speed of Answer									
F 4.1	O-12 Region (seconds)	Party w Retail	392.25	5,402,010	17.00	74,387			YES
General - Maintenance Center									
Average Answer Time									
F 5.1	MAR-6 Region (seconds)	Party w Retail	53.04	1,846,767	27.80	85,878			YES
General - Operator Services (Toll)									
Average Speed to Answer									
F 6.1	OS-1 KY (seconds)	PBD	3.70						PBD
% Answered in 10 seconds									
F 6.2	OS-2 KY (%)	PBD	86.30%						PBD
General - Directory Assistance									
Average Speed to Answer									
F 7.1	DA-1 KY (seconds)	PBD	4.76						PBD
% Answered in 12 seconds									

BellSouth Monthly State Summary Kentucky, December 2002

		Benchmark/ Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
F 7.2	DA-2	KY (%)	PBD	89.00%						PBD
General - E911										
F 8.1	E-3	Mean Interval KY (hours)	PBD	0.83	1,877					PBD
F 8.2	E-2	% Accuracy KY (%)	PBD	97.38%	90,239					PBD
F 8.3	E-1	% Timeliness KY (%)	PBD	100.00%	1,877					PBD
General - Billing										
Usage Data Delivery Accuracy										
F 9.1	B-3	Region (%)	Party w Retail	98.25%	3,431	100.00%	29,399	0.00236	-7.3806	YES
F 9.2	B-5	Usage Data Delivery Timeliness Region (%)	Party w Retail	99.65%	26,116	99.34%	741,517,597	0.00036	8.4765	NO
F 9.3	B-4	Usage Data Delivery Completeness Region (%)	Party w Retail	99.87%	26,116	99.91%	741,517,597	0.00023	-2.1125	YES
F 9.4	B-6	Mean Time to Deliver Usage Region (days)	Party w Retail	3.35	26,116	2.20	741,517,597			YES
Recurring Charge Completeness										
F 9.5.1	B-7	Resale/KY (%)	Party w Retail	92.76%	\$2,570,605	97.95%	\$97,583	0.00314	-14.6196	YES
F 9.5.2	B-7	UNE/KY (%)	>= 90%			\$118,306				YES
F 9.5.3	B-7	Interconnection/KY (%)	>= 90%			\$2,737				YES
F 9.6.1	B-8	Resale/KY (%)	Party w Retail	87.33%	\$4,066,817	99.17%	\$123,154	0.00270	-43.8158	YES
F 9.6.2	B-8	UNE/KY (%)	>= 90%			\$135,775				YES
F 9.6.3	B-8	Interconnection/KY (%)	>= 90%			\$56,214				YES
General - Change Management										
% Software Release Notices Sent On Time										
F 10.1	CM-1	KY (%)	>= 95% w in 30 days							
Average Software Release Notice Delay Days										
F 10.2	CM-2	KY (average)	>= 22 days prior to release							
F 10.3	CM-3	% Change Management Documentation Sent On Time KY (%)	>= 95% w in 30 days	100.00%	1					YES
F 10.4	CM-3	% Change Management Documentation (Defects, Corrections, etc.) Sent On Time KY (%)	>= 95% w in 5 days							
Average Documentation Release Delay Days										
F 10.5	CM-4	KY (average)	>= 22 days prior to release							
F 10.6	CM-5	% CLEC Interface Outages Sent within 15 Minutes KY (%)	>= 97% w in 15 min	94.12%	17					NO
F 10.7	CM-6	% Software Errors Corrected within 10 Business Days Region (%)	>= 95% w in 10 days	100.00%	1					YES
F 10.8	CM-6	% Software Errors Corrected within 30 Business Days Region (%)	>= 95% w in 30 days	26.00%	8					NO

BellSouth Monthly State Summary Kentucky, December 2002

	Benchmark/ Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
F:10.9	CM-6 Region (%)	>= 95% w in 45 days	33.33%	6					NO
F:10.10	% Change Requests Accepted or Rejected within 10 Business Days CM-7 Region (%)	>= 95% w in 10 days	100.00%	10					YES
F:10.11	% Change Requests Rejected Within The Reporting Period CM-8 Region (%)	Diagnostic	50.00%	10					Diagnostic
F:10.12	Number of Severity 1 Defects (Type 6 CR) in a Production Release Implemented CM-9 Region (number)	= 0 Defects	0						YES
F:10.13	Number of Severity 2 Defects (Type 6 CR) in a Production Release Implemented CM-9 Region (number)	= 0 Defects	2						NO
F:10.14	Number of Severity 3 Defects (Type 6 CR) in a Production Release Implemented CM-9 Region (number)	= 0 Defects	3						NO
F:10.15	% Fast Deck Weight Failure in Production Release CM-10 Region (%)	<= 5%	2.00%	100					YES
F:10.16	% Type 4 Change Requests (Implemented Within 60 Weeks) of those Implemented CM-11 Region (%)	>= 95% w in 60 days							
F:10.17	% Type 5 Change Requests (Implemented Within 60 Weeks) of those Implemented CM-11 Region (%)	>= 95% w in 60 days							
General - New Business Requests									
F:11.1	% New Business Requests Processed within 30 Business Days BFR-1 Region (%)	>= 90% w in 30 bus days	100.00%	2					YES
F:11.2	% Quotes Provided within 10 Business Days BFR-2A Region (%)	>= 90% w in 10 bus days							
F:11.3	% Quotes Provided within 30 Business Days BFR-2B Region (%)	>= 90% w in 30 bus days							
F:11.4	% Quotes Provided within 60 Business Days BFR-2C Region (%)	>= 90% w in 60 bus days							
General - Ordering									
Acknowledgment Message Timeliness									
F:12.1.1	O-1 EDI/Region (%)	>= 95% w in 30 min	100.00%	209,514					YES
F:12.1.2	O-1 TAG/Region (%)	>= 95% w in 30 min	100.00%	512,272					YES
F:12.2.1	O-2 EDI/Region (%)	100%	100.00%	209,516					NO
F:12.2.2	O-2 TAG/Region (%)	100%	100.00%	512,273					NO
General - Database Updates									
Average Database Update Interval									
F:13.1.1	D-1 LIDB/KY (hours)	PBD	0.40	21	0.40	21			PBD
F:13.1.2	D-1 Directory Listings/KY (hours)	PBD	0.04	26	0.04	26			PBD
F:13.1.3	D-1 Directory Assistance/KY (hours)	PBD	3.06	22	3.05	22			PBD
% Update Accuracy									
F:13.2.1	D-2 LIDB/KY (%)	>= 95%	100.00%	1,502					YES
F:13.2.2	D-2 Directory Listings/KY (%)	>= 95%	100.00%	816					YES
F:13.2.3	D-2 Directory Assistance/KY (%)	>= 95%	99.34%	760					YES
F:13.3	% NXXs / LRNs Loaded by LERG Effective Date D-3 Region (%)	100%	100.00%	19					YES

**BellSouth Monthly State Summary
Kentucky, December 2002**

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General - Network Outage Notification		Benchmark/ Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
Mean Time to Notify CLEC of Major Network Outages	Partly w Retail		464	1	445	1				YES
MR-7 (Region (minutes))										